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(11) **EP 0 665 590 B1**

(12) **EUROPEAN PATENT SPECIFICATION**

(45) Date of publication and mention  
of the grant of the patent:  
22.09.1999 Bulletin 1999/38

(51) Int Cl.<sup>6</sup>: **H01L 23/00, H02N 1/00,  
G01B 7/00, H01H 59/00,  
H02K 57/00, G01B 7/34**

(21) Application number: **95300521.2**

(22) Date of filing: **27.01.1995**

(54) **Process for manufacturing a microstructure**

Herstellungsverfahren für ein mikromechanisches Element

Procédé de fabrication d'une microstructure mécanique

(84) Designated Contracting States:  
**CH DE FR GB LI NL**

(30) Priority: **31.01.1994 JP 900494  
08.06.1994 JP 12596794  
09.09.1994 JP 21586194**

(43) Date of publication of application:  
**02.08.1995 Bulletin 1995/31**

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## Description

[0001] The present invention relates to a process of producing micro-structure.

[0002] In particular it concerns a microstructure of the type in which a beam is suspended over the surface of a substrate by a support. There is an air-gap between the beam and the substrate and no part of the beam is directly supported from below. An example of such a microstructure is disclosed in Japanese Patent Application JP-A-5116757 and Patent Abstracts of Japan, Vol. 17, No. 486 (M-1473) of 3 September 1993.

[0003] In recent years, micro-machines having small movable parts have been investigated. In particular, in the case of making microstructures by using technologies for semiconductor integrated circuits, such as those which include photolithographic processing, micro-parts can be reproduced accurately. Thereby, the parts can be arrayed easily on a substrate and the parts can be produced at low cost. The parts can respond quicker than parts produced by prior techniques because of their reduced size.

[0004] Three typical processes of producing a micro-structure on a substrate are described below.

[0005] One is a process for producing a micro-motor (M. Mehregany et al, "Operation of micro fabricated harmonic and ordinary side-drive motors" - Proceedings IEEE Micro Electro-Mechanical Systems Workshop 1990, pp. 1-8) or a linear microactuator (P. Chenung et al, "Modelling and position-detection of a polysilicon linear microactuator", Micro mechanical Sensors, Actuators and Systems ASME 1991, DS C-Vol.32, pp. 269-278).

[0006] This first process comprises: forming a silicon dioxide layer and a polysilicon layer on a silicon substrate in this order, or providing a SOI (silicon on insulator) substrate;

    patterning the silicon layer or the polysilicon layer to form a patterned structure; and  
 removing the silicon dioxide layer by an aqueous solution of hydrogen fluoride to produce the micro-structure. The silicon dioxide layer is used, therefore, as a sacrificial layer. (See "SOI (SIMOX) as a substrate for surface micromachining of single crystalline silicon sensors and actuators". The 7th International Conference on Solid State Sensors and Actuators, Transducers 93, June 7-10, 1993, pp. 233-236).

[0007] According to this first process, however, it is necessary to use for the micro-structure, materials which are not eroded by hydrogen fluoride, and it is not possible to incorporate an electrode of erodable material, such as an aluminium electrode in the micro-structure.

[0008] Furthermore, if polysilicon is adopted as the micro-structure material, it is necessary to regulate the

internal stress of polysilicon film in order to prevent a bending of the substrate.

[0009] Furthermore, in the case of using an SOI substrate, buried silicon dioxide is removed. If the removal of this oxide is not carefully controlled, over-etching will occur. It is difficult therefore to maintain contact between the microstructure and the substrate. Also, if aluminium or other metal is deposited after removal of the buried silicon oxide, it is not easy to form a continuous electrode structure due to overhang of the microstructure.

[0010] The second process is a process for producing a spatial light modulator device provided with an aluminium micro-mirror. This is described in Japanese Patent Laid-Open Application No. 2-8812. This process comprises coating a photo-resist sacrificial layer on a substrate, then forming an aluminium layer on this sacrificial layer, patterning the aluminium layer, and removing the sacrificial layer by oxygen plasma etching to produce an aluminium film micro-structure.

[0011] The micro-structure can be produced on various kinds of substrate and this does not depend on surface roughness of the substrate.

[0012] In addition, since the sacrificial layer is removed by dry etching, here a oxygen plasma etching process, sticking between the substrate and the micro-structure, which can happen when removing the sacrificial layer by a wet etching process, is avoided. However, since it is necessary to deposit the film for the micro-structure at a low temperature to avoid damage to the sacrificial layer, the choice of microstructure material is severely restricted. Furthermore, it is necessary to regulate the internal stress of the film for the microstructure to prevent the microstructure from bending.

[0013] The third process is a process in which the pattern for the microstructure is formed on an Si substrate, after which a part of the pattern is bonded anodically to a glass substrate, after which the bonded Si substrate is etched from its back surface until the pattern is left on the glass substrate.

[0014] A linear actuator comprising bulk Si film (Y. Giachandani et al, "Micron-Sized, High Aspect Ratio Bulk Silicon Micromechanical Systems Devices", Proceedings IEEE Electro Mechanical Workshop 1992, pages 208-213), or a cantilever comprising silicon nitride for an Atomic Force Microscope (AFM) may be produced by this process (U.S. Patent 5,221,415).

[0015] In this process, it is not necessary to use a sacrificial layer so that micro-structures made of a material which does not have resistance to hydrogen fluoride can be produced.

[0016] However, the microstructure materials are limited to those which can be bonded anodically to glass, such as Si, Al, Ti, Ni, which are electroconductive and which can be oxidised, or silicon dioxide film or silicon nitride film coated on a substrate.

[0017] Furthermore, when bonding is made anodically a temperature of 300°C or more is usual and it is necessary therefore to use a glass having the same thermal

expansion coefficient as that of the Si substrate to avoid damage to the substrate by heat stress. The choice of glass is limited to pyrex glass (trade name # 7740; manufactured by Corning) or the like.

[0018] Where an electrode is already provided on the substrate, it is then difficult to produce an electrode on the microstructure.

[0019] In addition, it is necessary to use a glass which contains mobile ions as the material of the substrate, such as soda glass, Pyrex and crystallised glass. Consequently, this process is inapplicable to substrates incorporating integrated circuit components.

[0020] Furthermore, in the case of bonding the electro-conductive material to the glass anodically, it is necessary for the glass and the electro-conductive material to have a surface roughness of 50 nm or less.

[0021] In US Patent 5,221,415, silicon nitride is bonded anodically to glass at 475°C. Consequently, electrodes have to be formed by vacuum evaporation on the whole surface of the substrate after producing the microstructure. It is difficult, however, to form a patterned electrode on a beam structure such as a cantilever.

#### SUMMARY OF THE INVENTION

[0022] The present invention is intended as a solution to the problems aforesaid.

[0023] According to the present invention, there is provided a process of producing a microstructure having a substrate, a beam member, and a supporting means, wherein the beam member is suspended over the surface of the substrate by said supporting means and is separated from said surface by an air-space, which process comprises performing in the following order steps of:

- (a) providing a first substrate having a first sacrificial layer over its surface and a beam member on said first sacrificial layer;
- (b) removing that part of said first sacrificial layer which is not sandwiched between said beam member and said substrate;
- (c) forming supporting means on said substrate and over said beam member with the inside surface of said supporting means attached to the surface of said first substrate and to that surface of said beam member which is further from the surface of said first substrate; and
- (d) removing the remainder part of said first sacrificial layer to leave said beam member separated from the surface of said first substrate by an air-space with no part thereof directly supported from below.

[0024] It is mentioned that European Patent Application EP-A-0417523 describes the manufacture of a microstructure by a process of forming a spacer layer, a first metal layer and a second metal layer, in that order,

on the surface of a silicon substrate. The second metal layer is etched leaving a central portion spaced from a surrounding peripheral part of the second metal layer. The same pattern is formed thereafter in the first metal layer except for a part that is left to support the central portion from below. The spacer layer is then etched leaving a peripheral part supporting the patterned first and second layers of metal. At the periphery a part of the first and second layers of metal overhang from the spacer layer and are separated from the substrate by an air-gap. The peripheral parts of the first and second metal layers are supported from below by the peripheral part of the spacer layer.

[0025] Other aspects of the invention will be apparent from the description given below and from the appended claims.

#### BRIEF DESCRIPTION OF THE DRAWINGS

[0026] In the accompanying drawings:

Figure 1 is a schematic view of a first microstructure;

Figures 2(A)-2(K) are sectional views for illustrating a first process for producing the microstructure of Figure 1, taken on line A-A of Figure 1;

Figure 3 is a schematic view of an electrostatic actuator;

Figures 4(A)-4(L) are sectional views, which are taken on line B-B of Figure 3, for illustrating a first process for producing the electrostatic actuator of Figure 3;

Figure 5 is a sectional view for explaining the use of an L-B film in a process for producing a microstructure;

Figures 6(A)-6(M) are sectional views for illustrating a second process for producing the electrostatic actuator of Figure 3;

Figure 7 is a schematic sectional view for explaining a step of bonding a silicon substrate to a second substrate anodically;

Figures 8(A)-8(M) are sectional views for illustrating a third process for producing the electrostatic actuator of Figure 3;

Figures 9(A)-9(K) are sectional views for illustrating a second process for producing the microstructure of Figure 1; and

Figures 10(A)-10(M) are sectional views for illustrating a fourth process for producing the electrostatic actuator of Figure 3.

#### DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

[0027] The present invention will be explained with reference to the drawings. The following description is given by way of example only.

[0028] Fig. 1 is a schematic view of a first micro-structure.

ture, and Fig. 2(A)-(K) are views illustrating stages in a process of producing the microstructure of Fig. 1.

[0029] Referring to figure 1, 1 is a first substrate, 2 is a beam member and 3 is a supporting structure. The member 2 is suspended above the first substrate 1, over an airspace 4, by the supporting structure 3, and the beam 2 is attached to the supporting structure 3 at its surface 5 which is further from the substrate 1.

[0030] A process for producing the microstructure shown in figure 1 will now be explained with reference to figure 2.

[0031] First of all, the first substrate 1 and a second substrate 6 are provided, and then a first sacrificial layer 7 is formed on the first substrate 1 (Fig. 1(A)).

[0032] Photo-resist is coated on the second substrate 6 to form a photoresist layer, after which the photo-resist layer is exposed to radiation and is developed to form a patterned photo-resist layer 8 (Fig. 2(B)).

[0033] The second substrate 6 is then etched using the patterned photo-resist layer as a mask to produce a patterned prominence 9 which is to form the beam member 2 (Fig. 2(C)).

[0034] The first substrate 1 having the first sacrificial layer 7 is then adhered to the second substrate 6 with the patterned prominence 9 in contact with the first sacrificial layer 7 (Fig. 2(D)).

[0035] The second substrate 6 is then etched down to the patterned prominence 9 to produce the beam-shaped member 2 (Fig. 2(E),(F)), after which the first sacrificial layer 7 is etched using the member 2 as a mask (Fig. 2(G)).

[0036] Next, a layer 10 for the supporting structure 3 is formed on the member 2 and on the exposed adjacent surfaces of the first sacrificial layer 7 and the first substrate 1 (Fig. 2(H)).

[0037] Next, a patterned photo-resist layer 11, whose pattern corresponds to the pattern of the supporting structure 3, is formed on the layer 10 (Fig. 2(I)) and the layer 10 is etched using the patterned photo-resist layer 11 as a mask to form the supporting structure 3 (Fig. 2(J)). The first sacrificial layer 7 is then removed to complete the manufacture of the microstructure shown in Fig. 1.

[0038] In the process shown in Figure 2, the first sacrificial layer 7 acts as an adhesive layer, and for this purpose may comprise resin. The first sacrificial layer can be formed by usual methods, such as by applying a coating solution in which resin is dissolved in an organic solvent, by spin coating, spray coating, dip coating etc., or by forming a film by the Langmuir-Blodgett method. A resin film having a flat surface can be obtained by these methods, even if the substrate has a rough surface, therefore in the step for adhering the first substrate to the second substrate, face-adhering can be effected without depending on surface roughness of the substrate.

[0039] Preferred resin materials include photo-resists. These should not contain impurities, such as so-

dium ions, if the first substrate is a silicon substrate including integrated circuit components. In particular, such photo-resists containing a rubber having both good adhesive strength and good mechanical strength are preferably used. As the rubber, cyclized rubbers such as disclosed in "Bisaikako & Rejisuto (Fine Processing and Resist)" written by Saburo Nonogaki, edited by Kobunsi Gakkai (Society of Polymer), published by Kyoritsu Shuppan, 1991, pages 11-12 are preferably used, and as the photoresists containing rubber, OMR-83 (manufactured by Tokyo Oka Kogyo K.K.) is preferably used.

[0040] In the case of using the Langmuir-Blodgett method for forming the first sacrificial layer 7, a monomolecular laminated film, that is a Langmuir-Blodgett (LB) film consisting of molecules with hydrophobic group and a hydrophilic group, is laminated on the first substrate 1, at least one layer or more, to form the sacrificial layer 7. In a multilayer film, the hydrophobic groups of each monomolecular film are adjacent and the hydrophilic group of each mono-molecular film are adjacent.

[0041] According to this method, the thickness of the sacrificial layer 7 can be regulated to nanometre precision, thus the air space 4 between the beam member 2 and the first substrate 1 can be regulated to the same nanometre precision.

[0042] As the step for adhering the first substrate 1 and the second substrate 6, the process as described below is preferred, that is, pressing together the first substrate 1 with the sacrificial layer 7 and the second substrate 6, and then heating the sacrificial layer 7 to evaporate solvent in the sacrificial layer 7 to cure the resin therein and to increase adhesive strength. In addition, at least one of the first substrate 1 and the second substrate 6 may be provided with a groove to allow solvent vapour to escape via the groove. Where the first substrate 1 and the second substrate 6 are conductive, it is possible to adhere the first and second substrates 1,6 while applying voltage between these substrates 1,6 to generate an electrostatic field.

[0043] In case of adhering the first and the second substrates 1,6 by an LB film, the substrates 1,6 can be adhered by the process as described below, that is a LB film formed on the first substrate 1 (or the second substrate 6) is brought into contact with the surface of the second substrate 6 (or the first substrate 1), and then a voltage is applied between the substrates 1,6 to generate an electrostatic field to adhere them to each other.

[0044] Further, in case of forming LB film on both the first and the second substrates 1,6, the LB films preferably are formed so that the hydrophilic group of one LB film comes into contact with hydrophilic group of the other LB film, or the hydrophobic group of one LB film comes into contact with the hydrophobic group of the other LB film.

[0045] Furthermore, in the case of forming the LB film on one only of the first and second substrates 1,6, when

the surface of the LB film is hydrophilic, the surface of the substrate is chosen to be hydrophilic, and when the surface of the LB film is hydrophobic, the surface of the substrate is chosen to be hydrophobic. Provided that the temperature of the LB film is maintained below its decomposition temperature, and a voltage is applied between the first and the second substrates 1,6 the substrates 1,6 can be adhered firmly.

[0046] In the process described above, the first substrate 1 can be adhered to a patterned prominence for the microstructure by a LB film. In case of using a resin layer for the sacrificial layer 7 as an adhesive layer, the resin layer can be cured at a relatively low temperature so that damage to the substrate 1 due to difference of thermal expansion coefficients between the first and second substrates 1,6 can be avoided. Thus, the choice of materials for the substrates 1,6 is not restricted in relation to thermal expansion coefficients.

[0047] In the steps for making the patterned thin film structure, such as the beam-shaped member 2 in Figure 1, on the first substrate 1, a second substrate 6 or a second substrate 6 having a layer for the structure also may be processed.

[0048] Examples of the second substrate 6 having a layer for the structure may include a substrate 6 on which this layer is formed directly or a substrate 6 on which the layer is formed over said sacrificial layer for releasing the substrate 6 from the layer.

[0049] In case of forming the patterned thin film layer from the second substrate 6, first of all, the second substrate 6 is processed by a suitable method, such as by photolithography, to form a patterned structure, after which the second substrate 6 is attached to the first substrate 1 by the first sacrificial layer 7 with the patterned surface of the second substrate 6 facing the first substrate 1. The second substrate 6 is then etched down to the patterned prominence 9 from the opposite surface to the patterned prominence 9 (i.e. the back surface) by wet etching with a suitable etchant for the second substrate 6, or by dry etching with a reactive gas, or the back surface of the second substrate 6 is abraded and lapped to form the patterned thin film structure 2.

[0050] In case of using a silicon substrate as the second substrate 6, examples of etchants for wet etching include alkali aqueous solutions, such as a solution of potassium hydroxide (KOH), a solution of tetramethylammonium hydroxide (TMAH), etc., or mixed aqueous solutions of hydrogen fluoride and nitric acid, and examples of reactive gases for dry etching include plasma gases of  $CF_4$ ,  $SF_6$ ,  $NF_3$  etc.

[0051] When a substrate made of monocrystalline Si, GaAs etc. is used as the second substrate 6, the microstructure obtained is not warped.

[0052] In the case of forming the patterned thin film layer 2 from the second substrate 6 having a layer for the structure 2, examples of the second substrates 6 include a laminated substrate which a glass substrate is bonded anodically to a metal layer as the second sacri-

ficial layer formed on the layer for the structure, a substrate having an internal layer, such as SOI substrate which has silicon oxide film as the second sacrificial layer and silicon layer as the layer for the structure, SIMOX substrate etc., a substrate on which the second sacrificial layer and the layer for the structure is formed directly.

[0053] Examples for the material of the second sacrificial layer are selected from materials which are eroded by an etchant which does not erode the layer for the structure 2.

[0054] The second sacrificial layer and/or the layer for the structure may be produced by a film forming method, such as vacuum evaporation, liquid coating and chemical vapour deposition and so on.

[0055] In the case of using anodic bonding for bonding the glass substrate and the metal film as the second sacrificial layer, the glass substrate preferably may contain mobile ions of alkali metal, such as sodium ions, lithium ions, etc. The metal film preferably may be of metal which can be bonded anodically, such as Al, Ti, Ni and so on, or of an alloy which contains a combination of these metals. Anodic bonding, such as may be used herein, is described, for example in U.S. Patent 3,397,278.

[0056] When the metal film as above is formed on the layer for the structure, the layer used may be chosen from various materials, such as insulator semiconductor, or metal material.

[0057] In the case of using the second substrate having a layer for the structure, it is necessary to remove the second substrate from the layer for the structure.

[0058] The second substrate can be removed from the layer by wet etching using an etchant suitable for the material of the second substrate, dry etching by a reactive gas, or abrasion.

[0059] In the case of using a second substrate having a second sacrificial layer, the second substrate can be removed from the layer by removing the second sacrificial layer to transfer the layer for the structure on the first sacrificial layer which had been formed on the first substrate.

[0060] The first sacrificial layer may comprise one layer or two or more layers, and in case of forming the first sacrificial layer comprising two or more layers, each layer may be cured before forming the other layer on the layer to avoid dissolution of the layer.

[0061] The curing can be practised by use of usual methods, such as baking, irradiation and so on.

[0062] In the case of using photoresist for the first sacrificial layer, the curing temperature may be the temperature at which the resin in the photoresist becomes crosslinked and insoluble to developer.

[0063] When negative type photoresist is used as the photoresist, the part which is irradiated is cured. In this curing step organic solvent vapour is evaporated from the photoresist so that it is preferable to provide a groove on the first substrate and/or second substrate to allow

the vapour to escape, as described above.

[0064] The supporting structure 3 connects the first substrate and the layer for the structure, and is formed before removing the first sacrificial layer to provide support for the layer at the upper surface of the layer.

[0065] In addition, in case of using aluminium for the supporting structure, the layer can be connected with the first substrate electrically.

[0066] The first sacrificial layer can be removed by usual methods, such as immersion in a solvent for dissolving resin, dry etching by ashing with oxygen plasma etc. Further, in case of using LB film as the first sacrificial layer, the first sacrificial layer can be removed by heating to decompose the LB film. Examples of heating the LB film may include irradiating by laser beam, such as from a CO<sub>2</sub> laser.

[0067] When resin is used for the first sacrificial layer, it is possible to remove the first sacrificial layer by dry etching so that sticking, which can happen, when the first sacrificial layer is removed by wet etching, can be avoided.

[0068] As described above, in the process for producing the microstructure the choices of the first substrate material, the second substrate material and the material for the layer for the structure are not restricted. In addition, in the process for removing the first sacrificial layer, electrodes can be provided on the structure. Provided the first sacrificial layer is removed by using solvent, by ashing, by heating etc., electrodes are not etched.

[0069] Furthermore, sticking can be avoided by removing the first sacrificial layer and the second sacrificial layer by dry etching process or heating.

[0070] Examples of the microstructure may include: cantilevers for microscopes which detect tunnelling electric current, Van der Waals' force, magnetic force or electrostatic force etc., such as atomic force microscopes (AFM); scanning tunnelling microscopes (STM); and electric circuits having air bridge structure, etc.

[0071] In particular, an electrostatic actuator comprising a substrate, fixed electrode and first plate electrode which are formed on the substrate, a beam provided on the first plate electrode over an air space by a supporting structure comprising a metal film and a second plate electrode formed on the beam, wherein the electrode is connected electrically with the fixed electrode by the supporting structure, and the beam is supported by the supporting structure at the upper surface of the beam, and the beam is movable by applying voltage between the first plate electrode and the movable electrode can be obtained.

[0072] A number of embodiments of the present invention will now be explained in more detail with reference to the following examples.

#### EXAMPLE 1

[0073] A microstructure shown in Figure 1 was prepared according to a process as shown in Figure 2.

[0074] First of all, a monocrystalline silicon substrate was provided as a second substrate 6.

[0075] A layer of photoresist was coated on this silicon substrate 6. The photoresist layer was then exposed and developed to produce the patterned photoresist layer 8 whose pattern corresponds to the pattern of the beam-shaped member 2.

[0076] Next, the silicon substrate 6 was etched by reactive ion etching with a mixture of SF<sub>6</sub> and CCl<sub>2</sub>F<sub>2</sub> using the patterned photoresist layer 8 as a mask to produce a beam patterned prominence 9. The patterned photoresist layer 8 was then removed (Fig. 2(C)).

[0077] The height of the beam patterned prominence 9 was 5 μm.

[0078] Meanwhile, a glass substrate (trade name: # 7059; manufactured by Corning) was provided as the first substrate 1.

[0079] Polymethylmethacrylate (PMMA) was dissolved in methylethylketone (MEK) to make a PMMA solution, after which the PMMA solution was coated on the glass substrate 1 by spin coating to form the first sacrificial layer 7 of PMMA film. The thermal expansion coefficient of the glass substrate 1 was 1.4 times that of the silicon substrate 6.

[0080] Next, the second substrate 6 shown in figure 2(C) was adhered to the surface of the first substrate 1 by the first sacrificial layer 7 by applying pressure to the back surface of each substrate 1, 6.

[0081] Pressure was applied to ensure that the beam patterned prominence 9 was adhered adequately to the first sacrificial layer 7.

[0082] The first sacrificial layer 7 was then cured by heating at 150°C. and the thickness of the cured first sacrificial layer 7 was 2 μm.

[0083] The silicon substrate 6 was then etched with a 30 wt% KOH aqueous solution heated at 100°C. to form a thin silicon membrane 12 as shown in Figure 2(E), after which the silicon thin film was removed by reactive ion etching with SF<sub>6</sub> gas to produce the beam-shaped member 2 having 1 μm of thickness (Figure 2(F)). The first sacrificial layer 7 was then etched by reactive ion etching with oxygen using the beam-shaped member 2 as a mask (Figure 2(G)).

[0084] Next, an aluminium layer 10 was formed on the exposed surfaces of the first substrate 1, first sacrificial layer 7 and member 2 by sputtering. The thickness of the aluminium layer 10 was 1 μm. Then a patterned photoresist layer 11 was formed on the aluminium layer 10 (Figure 2(I)). The aluminium layer 10 was then etched by an etchant comprising acetic acid, phosphoric acid and nitric acid to produce the supporting structure 3 (Figure 2(J)).

[0085] Finally, the first sacrificial layer 7 was removed by immersing the first substrate 1 into an organic solvent for removing PMMA (trade name: OMR Remover, manufactured by Tokyo Ouka Kogyo K.K.) to complete the microstructure comprising a 1 μm thickness silicon beam 2 supported by aluminium supporting structure 3,

and having a 2  $\mu\text{m}$  air space 4 between the beam 2 and the first substrate 1.

[0086] Since an organic solvent was used to remove the first sacrificial layer 7, the supporting structure 3 was not etched.

[0087] According to Example 1, the bridge member 2 was provided by a part 9 of the second substrate 6 made of crystalline silicon as shown in Figure 2(B). A patterned beam member which does not have internal stress therefore can be prepared easily.

[0088] In addition, the second substrate 6 was grooved so that vapour evaporated in the step of curing the first sacrificial layer 7 could escape. If a groove is not provided, bubbles can be trapped between the first sacrificial layer 7 and the second substrate 6. When a groove is provided, bubbles can be avoided.

[0089] When a resin layer is used for the first sacrificial layer 7, the material of the first substrate 1 can be selected from materials whose thermal expansion coefficients are different from that of the second substrate 6.

[0090] Furthermore, in this example, the first sacrificial layer 7 serves not only as a spacer but also as an adhesive layer.

[0091] In this example, aluminium is used for the supporting structure 3 but insulator material, such as silicon dioxide etc., could be used instead to produce an insulated microstructure.

#### EXAMPLE 2

[0092] A microstructure shown in Figure 3 was prepared according to the process as shown in Figure 4.

[0093] Referring to Figure 3: 1 is a silicon substrate; 13 is an insulating layer of silicon dioxide; 14 is a first-plate electrode formed on the insulating layer 13; 15 is a fixed electrode formed on the insulating layer 13; 2 is a beam member having a torsion bar portion 2a and a cantilever portion 2b, and is suspended by a supporting structure 3 attached to the upper surface of the torsion bar portion 2a of the beam member 2. The beam member 2 is provided with a second plate electrode 16 and is spaced above the insulating layer 13 by an air space 4. (Hereinafter this structure is called an "air bridge structure").

[0094] The supporting structure 3 is of an electrically conductive material, and the second plate electrode 16 is connected to the fixed electrode 15 electrically by the supporting structure 3.

[0095] When a voltage is applied between the first plate electrode 14 and the fixed electrode 15 which is connected to the second plate electrode 16, the torsion bar portion 2a of the beam member 2 is twisted torsionally and the cantilever portion 2b of the beam member 2 is displaced. Thus, the microstructure of this example can be used as an electrostatic actuator.

[0096] Next, a process for preparing the microstructure as shown in Figure 3 will be explained with reference to Figure 4.

[0097] First of all, a silicon substrate was provided as the second substrate 6. Next, a 1  $\mu\text{m}$  thick silicon nitride layer 17 was formed on the silicon substrate 6 by Low Pressure Chemical Vapour Deposition (LP-CVD) using a gas mixture of dichlorosilane ( $\text{SiH}_2\text{Cl}_2$ ) and ammonia ( $\text{NH}_3$ ) at 850°C (Figure 4(A)).

[0098] A rubber type photoresist (trade name: OMR83: manufactured by Tokyo Ouka Kyogo K.K.) was coated on the silicon nitride layer 17 by spin coating to form a first photoresist layer 7a (Figure 4(B)).

[0099] Also, a silicon substrate 1 provided with an insulating layer 13, a first plate electrode 14 and a fixed electrode 15 (not shown in Figure 4) was prepared.

[0100] The insulating layer 13 was prepared by thermally grown oxidation of the silicon substrate 1 by an oxidising gas, and typically the thickness of this insulating layer 13 is 1  $\mu\text{m}$ . The first plate electrode 14 and the fixed electrode 15 were prepared by forming a 5 nm thickness Cr layer and a 20 nm thickness Au layer on the insulating layer 13 by electron beam deposition, and patterning by photolithography.

[0101] Next, a photoresist was coated on the first substrate to form a second photoresist layer 7b in the same manner as for the first photoresist layer 7a (Figure 4(C)).

[0102] The first substrate 1 and the second substrate 6 were then heated at 50°C for 10 minutes to regulate the amount of solvent in the photoresist layers 7a, 7b in order to avoid bubbles between the photoresist layers 7a, 7b when the photoresist layer 7a was adhered to the photoresist layer 7b.

[0103] The second substrate 6 was then placed on the first substrate 1 with the photoresist layers 7a, 7b therebetween and 100 V of voltage was applied between the first substrate 1 and the second substrate 6 to adhere the substrates 1, 6 by the photoresist layers 7a, 7b, after which the photoresist layers 7a, 7b were then heated at 120°C to cure the photoresist layers 7a, 7b and produced a consolidated photoresist layer 7 (fig. 4(D)). The cured consolidated photoresist layer 7 was 2  $\mu\text{m}$  thick.

[0104] Next, the second substrate 6 was removed by wet etching using 30 wt% of KOH aqueous solution to expose the silicon nitride layer 17 (figure 4(E)).

[0105] In this step, silicon nitride was not etched substantially since the etching speed of silicon nitride is much lower than that of silicon.

[0106] Next, a metal film 18 consisting of Cr and Au sub-layers was formed on the silicon nitride layer 17 in the same manner as for forming the first plate electrode 14 and the fixed electrode 15, and a patterned photoresist layer 19 was formed by photolithography (Figure 4(F)).

[0107] The metal film 18 was then etched by using the patterned photoresist layer 19 as a mask to form the second plate electrode 16, and then a patterned photoresist layer 20 whose pattern corresponded to the pattern of the beam member 2 was formed on the silicon nitride layer 17 (Figure 4(G)).

[0108] The silicon nitride layer 17 was then etched by reactive ion etching using  $\text{CF}_4$  gas with the patterned photoresist layer 20 as a mask. After this, the patterned photoresist layer 20 and exposed part of the photoresist layer 7 were ashed by reactive ion etching with oxygen gas and removed to complete production of the patterned beam member 2 (Figure 4(H)).

[0109] Next, aluminium was deposited on the exposed surface of insulating layer 13 on the exposed surface of silicon nitride layer and on the second plate electrode 16 to form an aluminium layer 10, after which a patterned photoresist layer 11 was formed on the aluminium layer 10.

[0110] The aluminium layer 10 was then etched by reactive ion etching using a gas mixture of  $\text{BCl}_3$  and  $\text{Cl}_2$  to form the supporting structure 3.

[0111] The patterned photoresist layer 11 and the patterned photoresist layer 7 were etched with oxygen plasma and an air space formed between the beam member 2 and the substrate 1 (Figure 4(L)). The concentration of the oxygen was 50 ccm or more and pressure of the gas was 20 Pa or more to promote isotropic etching.

[0112] In this manner, a microstructure comprising a 2  $\mu\text{m}$  air-space 4 and a 1  $\mu\text{m}$ -thick beam member 2 as shown in Figure 3 was prepared. In this example, a photoresist layer 7 was used as a first sacrificial layer. Each electrode was not etched and sticking, which would have happened if the sacrificial layer had been removed by wet etching, was avoided since dry etching was used.

[0113] Further, according to this embodiment of the present invention a silicon nitride layer formed on the second substrate was transferred onto the first substrate to form the beam member and a patterned plate electrode was formed on the beam member. This electrode was connected electrically to the fixed electrode formed on the first substrate by the supporting structure.

[0114] Furthermore, the free-end of the beam member, a cantilever portion, could be displaced toward the first substrate correspond to the torsional twisting of the torsion bar portion of the beam member by applying a voltage between the first and second plate electrodes.

#### EXAMPLE 3

[0115] Another embodiment of a process for producing the microstructure shown in Figure 1 will be explained with reference to Figure 5. In this example, a LB layer was used as the first sacrificial layer 7 in Example 1 and Figure 5 is a schematic view illustrating the step of adhering the first substrate to the second substrate by this first sacrificial layer. In this figure, 1 is a silicon first substrate, 6 is a silicon second substrate, 7 is a LB layer formed by LB technique on the first substrate 1, and the LB layer 7 consists of many LB film monomolecular sublayers 21, and the LB layer 7 is of thickness 80 nm. Further, 22 is a hydrophobic group and 23 is a hydrophilic group, and the LB film sub-layers 21 are laminated alternately with hydrophilic groups 23 adjacent

and with hydrophobic groups 22 adjacent, as shown. 24 is a power supply to apply a voltage between the first substrate 1 and the second substrate 6, and it is connected to needle electrodes 25 by conductive wires 26. 27 is an electroconductive platen.

[0116] In this example, stearic acid was used for forming the LB layer 7 on the first substrate 1, and then the second substrate 6 having a patterned surface (not shown) was placed on the first substrate 1 with the LB layer 7 inbetween.

[0117] Next, 6 V of voltage was applied between the first substrate 1 and the second substrate 6 for 30 minutes to adhere the second substrate 6 to the first substrate 1 by using electrostatic force which was generated by the voltage, and the hydrophobic surface of the LB layer 7 was adhered to the hydrophobic surface of the second substrate 6.

[0118] Next, the adhered substrates 1,6 were processed in the same manner as in the process of Example 1 as shown in Figures 2(E) to 2(J) to form a microstructure comprising an aluminium supporting structure 3 and a silicon beam member having the same structure as that shown in Figure 2(J), except in using an LB layer instead of a PMMA layer as the first sacrificial layer 7. The microstructure was then heated at 350°C for 1 hour to decompose and remove the LB layer 7, and a 80 nm air-space was formed between the first substrate 1 and the silicon beam member 2.

[0119] In this example, a microstructure having an air-space which is regulated to nm precision can be prepared by using an LB layer as the first sacrificial layer.

[0120] In addition, the LB layer can be removed by heating, so that sticking which can happen if the first sacrificial layer is removed by wet etching can be avoided.

[0121] Further, in this example, the LB layer 7 is formed on the first substrate 1. However, it can be formed on the second substrate 6, or can comprise sublayers formed on the first substrate 1 and the second substrate 6. Examples of removing the LB layer 7 may include irradiating by laser, such as  $\text{CO}_2$  laser to decompose the LB layer 7.

[0122] Furthermore, in this example, stearic acid was used for forming the LB layer 7, but arachidic acid, ferroelectric LB film, such as diacetylene type, benzene derivatives type, and polyimide LB film also can be used to form the LB layer 7 of the example.

[0123] Examples of the substrate 1 may include glass, metal, glass substrate on which metal film has been formed, etc.

#### EXAMPLE 4

[0124] Another embodiment for producing the microstructure shown in Figure 3 will be explained with reference to Figure 6.

[0125] First of all, a silicon substrate 28 was prepared, and a 200 nm thick aluminium layer 29 was formed on



the silicon substrate 28 as a second sacrificial layer by electron beam deposition (Figure 6(A)). Next, a glass substrate (trade name: No. 7740, mfd. by Corning) was prepared as the second substrate 6, after which the silicon substrate 28 having the aluminium layer 29 was bonded anodically to the glass substrate 6.

[0126] The bonding step will now be explained in detail with reference to Figure 7.

[0127] Referring to Figure 7, 24 is a power supply for applying voltage between the aluminium layer 29 and the glass substrate 6, and it is connected with needle electrodes 26 by connecting wires 26, and 27 is an electroconductive platen comprising a heating means (not shown).

[0128] In the bonding step, the second substrate 6 was placed on the aluminium layer 29, after which 500 V of voltage was applied between the second substrate 6 and the aluminium layer 29 for 20 minutes at a platen temperature of 300°C to bond the second substrate 6 to the silicon substrate 28 (Figure 6(B)). The silicon substrate 28 was then abraded down to a layer of 2 µm-thickness (Figure 6(C)).

[0129] Next, a patterned photoresist layer, whose pattern corresponded to a beam pattern, was formed on the silicon layer 28, after which the silicon layer 28 was etched by reactive ion etching using CF<sub>4</sub> gas, after which the aluminium second sacrificial layer 29 was etched by reactive ion etching using a gas mixture of BCl<sub>3</sub> and Cl<sub>2</sub> to form the beam member 2 having a cantilever portion (Figure 6(D)).

[0130] A silicon first substrate having an insulating layer 13, a first plate electrode 14, and a fixed electrode 15 was prepared. The insulating layer 13 had been formed in the same manner as in Example 2. In addition, the first plate electrode 14 and the fixed electrode 15 had been formed as follows: first of all, a 5 nm Cr layer and a 200 nm Au layer were formed on the insulating layer 13 by electron beam deposition, after which a patterned photoresist layer was formed on the Au layer, after which the Au layer was etched with an etchant comprising iodine and potassium iodide, and the Cr layer was etched with an etchant comprising cerium ammonium nitrate and perchloric acid.

[0131] Next, a rubber-type photoresist (trade name: OMR 83; mfd. by Tokyo Ouka Kogyo) was coated on the surface of the first substrate having the fixed electrode 15 and the first plate electrode 14 to form the first sacrificial layer 7 (Figure 6(E)).

[0132] The second substrate 6 having the beam member 2 was then pressed onto the first sacrificial layer 7, after which these substrates 1, 6 were heated at 150°C to evaporate the organic solvent in the photoresist layer 7 and to cure it to adhere the second substrate 6 to the first substrate 1. The cured photoresist layer 7 had 2 µm of thickness.

[0133] The aluminium second sacrificial layer 29 was then removed at 80°C by an etchant comprising phosphoric acid, nitric acid and acetic acid to release the sec-

ond substrate 6 (Figure 6(G)). The beam member 2 was thus transferred to the first sacrificial layer 7.

[0134] The first sacrificial layer 7 and the first substrate 1 were not etched by the aluminium etchant.

[0135] Next, the second plate electrode 16 was formed on the beam member 2 in the same manner as that for the fixed electrode 15 and the first plate electrode 14 (Figure 6(H)).

[0136] The first sacrificial layer 7 was then etched by reactive ion etching using oxygen-gas, and using the pattern of the beam as a mask (Figure 6(I)).

[0137] Next, aluminium was deposited on the insulating layer 13, the beam member 2 and the second plate electrode 16 to form 2 µm of aluminium layer 10 (Figure 6(J)), after which a patterned photoresist layer 11, whose pattern corresponds to the pattern of the supporting means 3, was formed on the aluminium layer 10 (Figure 6(K)). The aluminium layer 10 was then etched by reactive ion etching using a gas mixture of BCl<sub>3</sub> and Cl<sub>2</sub> to form the supporting means 3.

[0138] The photoresist layer 11 and the first sacrificial layer 7 were then removed by etching with oxygen plasma to form a microstructure having a space 4 (Figure 6(M)).

[0139] According to this example, a microstructure having a 2 µm air space 4 and a 2 µm-thickness silicon beam member 2 having a cantilever portion supported by an aluminium supporting structure 3 was prepared, and in addition each electrode 14, 15, 16 was not etched and sticking was avoided by using reactive ion etching.

[0140] According to the process for producing the microstructure in this example, a beam member 2 which does not have internal stress and which is not bent can be formed from processing the substrate 28 made of crystalline silicon, and the beam member can be connected electrically with the fixed electrode 15 formed on the first substrate 1 by the supporting structure 3.

[0141] Examples of the substrate 1 may include silicon, glass, GaAs, metal, a glass substrate on which a metal film has been formed, etc.

[0142] In the above step, where the silicon beam member 2 is adhered to the first substrate 1 by the first sacrificial layer 7, a voltage of 100 V can be applied between the beam member 2 and the first substrate 1 to generate electrostatic force, instead of applying pressure.

[0143] In the process according to Example 4, there is an alternative step for producing the beam member 2. This step is as follows:

[0144] First of all, an SOI substrate having a 2 µm-thickness silicon layer is prepared, after which the aluminium layer 29 is formed on the silicon layer. Next, the silicon layer is bonded anodically to the second substrate 6. In performing this step, the SOI substrate was etched with 30 wt% of KOH aqueous solution to remove the silicon base substrate of the SOI substrate. The silicon dioxide layer acts as an etch stop layer so that once the silicon substrate is removed, the etching is stopped.

Next, the silicon dioxide layer is removed by hydrogen fluoride aqueous solution to produce a laminated second substrate comprising a 2  $\mu\text{m}$ -thick silicon layer 28 on the second sacrificial layer 29 and the second substrate, the same structure as shown in Figure 6(c). In this case, the thickness of the layer for the beam member 2 can be guaranteed.

[0145] Alternatively, the SOI substrate can be used directly as a laminated second substrate as shown in Figure 6(C), in which case the buried thermally grown silicon dioxide serves as the second sacrificial layer instead of aluminium. The silicon layer of the SOI substrate is etched to form a pattern in the same manner as that shown in Figure 6(D), after which the patterned silicon film 2 is adhered to the first substrate 1 shown in Figure 6(E). The buried silicon dioxide is then removed with hydrogen fluoride to transfer the patterned silicon layer onto the first sacrificial layer 7 as shown in Figure 6(G).

[0146] In this example, aluminium film was used as the second sacrificial layer, but other metal which can be bonded anodically, such as Ti, Cr, Ni etc., and a selective etchant chosen which does not erode the first sacrificial layer 7 and the beam member 2. Furthermore, a first sacrificial layer 7 of resin can serve as an adhesive layer, in which case the layer 28 should be grooved so that solvent vapour evaporated can escape in the step of curing the first sacrificial layer 7 (Figure 6(F)).

#### EXAMPLE 5

[0147] Another process for producing the microstructure electrostatic actuator of Figure 3 will be explained with reference to Figure 8. First of all, a silicon substrate 30 was provided having a 1  $\mu\text{m}$ -thick thermally grown silicon dioxide layer 31 which is to form the beam member 2. Next, a 200 nm Cu layer and a 10 nm Al layer were formed on the silicon dioxide layer 31 by sputtering to form the second sacrificial layer 29 (Figure 8(A)).

[0148] The second sacrificial layer 29 was then bonded anodically to a second substrate 6 of glass (trade name: No. 7740; mfd. by Corning) in the same manner as that in Example 4 (Figure 8(B)). When the second sacrificial layer 29 was bonded anodically to the second substrate 6, the aluminium in the second sacrificial layer 29 was dispersed into the copper layer to form an alloy of aluminium and copper at the interface of the Al layer and the Cu layer. The silicon substrate 30 was then removed by dry etching using  $\text{SF}_6$  gas (Figure 8(C)).

[0149] This etching step was selective and therefore the silicon oxide layer 31 remained on the second substrate 6.

[0150] As the first substrate 1, a glass substrate (trade name: No. 7059; mfd. by Corning) was prepared, and the first plate electrode 14 and fixed electrode 15 (not shown in Figure 8) were formed on the first substrate 1 in the same manner as in Example 4. Next, a solution in which PMMA had been dissolved into MEK was coat-

ed on the electrode surface of the first substrate 1 to form the first sacrificial layer 7 (Figure 8(D)). The first substrate 1 was then heated at 50°C for 10 minutes to regulate the amount of the solvent in the first sacrificial layer 7 to prevent bubbles forming at the adhesive interface when the first substrate 1 is later adhered to the second substrate 6.

[0151] Next, the second substrate 6 as shown in Figure 8(C) was pressed onto the first substrate 1 shown in Figure 8(D) bringing the silicon dioxide layer 31 into contact with the first sacrificial layer 7 (Figure 8(E)).

[0152] The pressure was regulated in order that the silicon dioxide layer 31 was adhered adequately to the first sacrificial layer 7.

[0153] The first layer 7 was then cured at 150°C. The cured first sacrificial layer 7 had a 2  $\mu\text{m}$  thickness.

[0154] The second sacrificial layer 29 was then removed by an etchant selective for copper comprising  $\text{FeCl}_3$ , to release the second substrate 6, and to transfer the silicon dioxide film 31 onto the first sacrificial layer 7 (Figure 8(F)).

[0155] Next, a patterned photoresist layer was formed on the silicon dioxide layer 31, and the silicon dioxide layer 31 was then etched with hydrogen fluoride using the patterned photoresist layer as a mask to form the beam member 2; after which the patterned photoresist was removed by reactive ion etching with oxygen gas, and in this etching step for the photoresist, the first sacrificial layer 7 was also etched to form a patterned first sacrificial layer, whose pattern was the same as the pattern of the bridge member 2 (Figure 8(G)).

[0156] Next, the second plate electrode 16 was formed on the beam member 2 in the same manner as in Example 4 (Figure 8(H), (I)), after which a 2  $\mu\text{m}$ -thick aluminium layer 10 was formed (Figure 8(J)). Next, a patterned photoresist layer 11 was formed and the aluminium layer 10 was etched to form the supporting structure 3 in the same manner as in Example 4 (Figure 8(K), (L)), after which the patterned photoresist layer 11 and the first sacrificial layer 7 of PMMA were removed by immersion in an organic solvent for removing the photoresist (trade name: OMR Remover - 502; mfd. by Tokyo Ouka Kogyo K.K.) to prepare a microstructure having a 2  $\mu\text{m}$  air space 4 and a 1  $\mu\text{m}$ -thick silicon dioxide beam member 2 supported by an aluminium supporting structure 3.

[0157] Since an organic solvent was used the aluminium supporting structure 3 was not etched.

[0158] The microstructure prepared in this example had the same structure as the electrostatic actuator shown in Figure 3 except for using a glass substrate 1. The thermal expansion coefficient of glass is one order of magnitude larger than that of the thermally grown silicon oxide, but nevertheless a microstructure with a structure made of insulating material is formed on a substrate whose thermal expansion coefficient is different from that of the insulating material can be prepared.

[0159] In this example, as the first substrate 1, a glass

substrate was used, but other insulating material, such as quartz,  $Al_2O_3$ ,  $MgO$ ,  $ZrO_2$  etc., or semiconductor material, such as Si, GaAs, InP etc. or metallic materials can be used.

[0160] In addition, in this example, as the layer for the structure, silicon dioxide was used, but other insulating material, which can be processed as film, such as silicon nitride,  $Al_2O_3$ , APN etc. can be used.

[0161] Further, the layer 31 for the beam member 2 may be formed after forming the second sacrificial layer 29 on the second substrate 6. Furthermore, examples of the second sacrificial layer in Example 5 may include a layer which can be bonded anodically to the second substrate 6, such as an alloy layer of Cu and Al formed by sputtering.

[0162] In Example 5, the second sacrificial layer 29 was removed by wet etching, but it can be removed by dry etching with oxygen gas.

#### EXAMPLE 6

[0163] Another example of a process for producing a microstructure as shown in Figure 1 will be explained with reference to Figure 9. First of all, a substrate of crystalline silicon was prepared as a second substrate 6, and the second substrate 6 was etched to form a patterned prominence 9 of the beam 2 in the same manner as in Example 1 (Figure 9(A), (B)). The height of the prominence 9 was 5  $\mu m$ .

[0164] A glass substrate (trade name: No. 7059; mfd. by Corning) was prepared as a first substrate 1.

[0165] Next, a solution of a precursor of polyimide was coated on the first substrate 1 by spin coating, after which the first substrate 1 was heated at 250°C to harden the precursor to form a 2  $\mu m$ -thick polyimide layer 7c. Next, a solution in which PMMA had been dissolved into MEK was coated on the polyimide layer 7c to form a 0.5  $\mu m$ -thick PMMA layer 7d (Figure 9(C)).

[0166] In this example, the polyimide layer 7c and the PMMA layer 7d constituted a first sacrificial layer 7.

[0167] Next, the second substrate 6 was placed on the first substrate 1 with the prominence 9 in contact with the PMMA layer 7d, after which the substrates 1, 6 were heated at 150°C to cure the PMMA layer 7d, and to adhere the second substrate 6 to the first substrate 1 (Figure 9(D)). The first substrate 1 was then etched to form the beam member 2 on the PMMA layer 7d in the same manner as in Example 1, then using the beam member 2 as a mask, part of the first sacrificial layer 7 was removed by reactive ion etching using oxygen gas (Figure 9(G)).

[0168] Next, the aluminium supporting structure 3 was formed in the same manner as in Example 1 (Figure 9(H) to 9(J)).

[0169] Finally, the patterned photoresist layer 11 and the first sacrificial layer 7 were removed to prepare a microstructure comprising a silicon beam member 2 on the substrate 1 and having an air-space 4 between the

beam member 2 and the substrate 1 and the beam member 2 was suspended by the aluminium supporting structure 3.

[0170] According to this example, sticking could be avoided by using dry etching with oxygen plasma.

[0171] In addition, according to the process of this example, a beam member which does not have internal stress, and which is not bent can be prepared because of forming the beam member by processing crystalline silicon crystal to a thin layer. The silicon substrate 6 was provided with a groove beside the prominence 9 so that solvent vapour, evaporated in the step of curing the PMMA layer 7c, could escape.

[0172] If the surface area of the prominence 9 is large, the amount of the solvent in the resin layer may be regulated to avoid bubbles being trapped at the adhesive interface.

[0173] The amount of the solvent can be regulated by heating the resin layer at a low temperature at which the resin layer is not cured. It is possible to avoid bubbles in the 0.5  $\mu m$  thickness of PMMA layer by heating at 50°C for 10 minutes.

[0174] Furthermore, in this example, a resin layer (PMMA layer) was used as an adhesive layer so that the adhesive layer could be cured at 150°C. Thereby, it is possible to use, as the second substrate, a substrate whose thermal expansion coefficient is different from that of the first substrate.

[0175] Examples of the first substrate 1 may include an insulator, such as quartz,  $Al_2O_3$ ,  $MgO$ ,  $ZrO_2$  etc., a semiconductor, such as Si, GaAs, InP, or metallic materials.

[0176] According to this example, in the step for adhering the second substrate 6 to the first substrate 1, the prominence 9 is embedded into the PMMA layer 7d, but it is not embedded into the polyimide layer 7c as shown in Figure 9(D) so the polyimide layer acts as a layer for regulating the thickness of the air-space 4. By dividing the functions of the first sacrificial layer 7, firstly defining the air space, and secondly providing adhesion, by using a laminated layer structure, the air space can be regulated with good accuracy even if the substrate is warped and/or pressure is not applied uniformly when the second substrate 6 is adhered to the first substrate 1.

#### EXAMPLE 7

Test for repeatability of the space between the beam and the first substrate.

[0177] First of all 5 cm diameter glass substrate was provided as a first substrate 1. This glass substrate 1 was provided with a 200 nm-thick Cr layer on its surface, and it was warped due to the internal stress of the Cr layer.

[0178] Next, a 2  $\mu m$ -thick polyimide layer 7c and a 0.5  $\mu m$ -thick PMMA layer 7d were formed on the Cr layer in the same manner as in Example 6 to form a first sacri-

ficial layer 7.

[0179] In addition, a 5 cm diameter silicon wafer substrate was provided as a second substrate 6, and the second substrate 6 was provided with four beam members each produced in the same manner as in Example 6. Each pattern had a 100  $\mu\text{m}$ -width, a 20 mm-length and were separated at a 10 mm-pitch.

[0180] Next, by using the first substrate 1 and the second substrate 6, the first substrate 1 having four silicon beams on the PMMA layer 7d in the same manner as in Example 6, and as disclosed in Figure 9(D) to (G). The silicon beams 2 were then etched away, and the thickness of the first sacrificial layer 7 was measured. The thickness was within the range of 2-2.2  $\mu\text{m}$ .

[0181] On the other hand, another first substrate 1 having four silicon beams 2 was prepared in the same manner as described above except in forming a 2.5  $\mu\text{m}$ -thickness PMMA layer 7 instead of forming a 2  $\mu\text{m}$ -thickness of polyimide layer 7c and 0.5  $\mu\text{m}$ -thickness of PMMA layer 7d. Next, the beams 2 were etched away, and the PMMA layer thickness was measured. The thickness was in the range from 1.5  $\mu\text{m}$  to 2.4  $\mu\text{m}$ . This confirms that better accuracy is achieved by dividing the functions as aforesaid.

#### EXAMPLE 8

[0182] Another example of producing the microstructure of Figure 3 will be explained with reference to Figure 10.

[0183] A microstructure as shown in Figure 3 was prepared in the same manner as in Example 4 (Figure 6) except in using a first sacrificial layer 7 consisting of two photoresist layers 7c, 7d instead of a single photoresist layer.

[0184] The first sacrificial layer 7 in this example was prepared as follows:

a rubber type photoresist (trade name: OMR-83; mfd. by Tokyo Ouka Kogyo K.K.) was coated on the surface of the first substrate 1 including the fixed electrode 15 and the first plate electrode 14 to form a first photoresist layer 7c, after which the first photoresist layer 7c was heated at 80°C for 20 minutes to evaporate solvent in the photoresist layer, after which ultraviolet radiation was irradiated to the photoresist layer. The photoresist layer 7c was then heated at 150°C for 1 hour to cure it. Next, the same photoresist was coated on the first photoresist layer 7c to form 0.4 mm-thickness of a second photoresist layer 7d. In addition, the second photoresist layer 7d was cured when the second substrate 6 was adhered to the first substrate 1.

[0185] According to this example, in the step for adhering the second substrate 6 to the first substrate 1, the patterned part to form the beam member 2 is embedded in the second photoresist layer 7d, but it is not embedded in the first photoresist layer 7c as disclosed in Figure 10(F) so the first photoresist layer 7c works as a layer for regulating the air-space 4.

[0186] Therefore, in case of dividing the functions of the first sacrificial layer 7, one of which is a function as a layer for defining the thickness of the air space 4, and one of which is a function as a layer for adhering the first substrate 1 to the second substrate 6, into each function, the space can be regulated with a good accuracy even if the substrate 1 is warped and/or pressure is not applied uniformly when the second substrate 6 is adhered to the first substrate 1.

[0187] Further, in the step for adhering the second substrate 6 to the first substrate 1 (Figure 10(F)), it was possible to adhere the second substrate 6 to the first substrate 1 by applying 100 V of voltage between the second sacrificial layer 29 made of Al and the first substrate 1 made of silicon instead of applying pressure to the back surface of each substrate 1, 6. In the above case, the second sacrificial layer 29 was not etched in the step of preparing the beam member 2 (Figure 10(D)).

[0188] As described above:

(i) A microstructure comprising a substrate, a patterned structure provided on the substrate and spaced apart therefrom by an air-space, and a supporting structure which suspends the patterned structure can be obtained;

(ii) It is possible to obtain a microstructure made of various materials, such as insulators, metals and semiconductors;

(iii) It is possible to obtain a microstructure comprising a patterned structure on which is a patterned electrode connected electrically with the substrate of the microstructure, or with a fixed electrode formed on the substrate of the microstructure;

(iv) The material for a layer for the patterned structure, and the material for the first substrate are not limited because the step for processing the layer to form the structure can be performed independently.

(v) It is possible to obtain a microstructure comprising a patterned structure on a substrate over an air-space, wherein the structure is not warped if crystalline material is used as material for the structure;

(vi) If a resin layer is provided as the first sacrificial layer for forming a space between the substrate and the patterned structure, the first sacrificial layer can be removed by using a dry etching process, thereby sticking can be avoided, and a flat surfaced first sacrificial layer can be provided on the substrate without depending on the surface roughness of the substrate because resin layers can be made flat even if they are coated on uneven surfaces, for example, a substrate having a patterned electrode, therefore a second substrate can be adhered to the first substrate precisely;

(vii) A microstructure can be prepared by using a second substrate whose thermal expansion coefficient is different from that of the substrate of the microstructure because the process for producing

microstructure according to the above does not comprise a step in which the substrate is bonded to the second substrate at high temperature;  
 (viii) If the functions of the first sacrificial layer of spacing and adhesion are divided amongst component layers, the air-space between the substrate and the patterned structure of the microstructure can be regulated precisely.

# Claims

1. A process of producing a microstructure having a substrate (1), a beam member (2), and a supporting means (3), wherein the beam member (2) is suspended over the surface of the substrate (1) by said supporting means (3) and is separated from said surface by an air-space (4), which process comprises performing in the following order steps of:
  - (a) providing a first substrate (1) having a first sacrificial layer (7) over its surface and a beam member (2) on said first sacrificial layer (7);
  - (b) removing that part of said first sacrificial layer (7) which is not sandwiched between said beam member (2) and said substrate (1);
  - (c) forming supporting means (3) on said substrate (1) and over said beam member (2) with the inside surface of said supporting means (3) attached to the surface of said first substrate and to that surface (5) of said beam member (2) which is further from the surface of said first substrate; and
  - (d) removing the remainder part of said first sacrificial layer (7) to leave said beam member (2) separated from the surface of said first substrate by an air-space (4) with no part thereof directly supported from below.
2. A process according to claim 1 wherein step (a) includes:
  - (e) providing a second substrate (6) of material which is to form said beam member (2);
  - (f) producing a beam member patterned prominence (9) in the surface of said second substrate (6);
  - (g) attaching said beam member patterned prominence (9) to said first substrate (1) by means of said first sacrificial layer (7); and
  - (h) thinning said second substrate (6) to form said beam member (2) from said beam member patterned prominence (9).
3. A process according to claim 2 wherein said second substrate (6) is of crystalline material.
4. A process according to claim 1 wherein step (a) includes:
  - (i) providing a second substrate (6) having a precursor layer (17) thereon which is of material which is to form said beam member (2);
  - (j) attaching said precursor layer (17) to said first substrate (1) by means of said first sacrificial layer (7);
  - (k) removing said second substrate (6); and
  - (l) producing said beam member (2) by patterning said precursor layer (17).
5. A process according to claim 4 wherein said precursor layer (17) provided on said second substrate (6) is produced by thermal oxidation of said second substrate (6).
6. A process according to claim 4 wherein said precursor layer (17) provided on said second substrate (6) is attached thereto by means of a second sacrificial layer (29), and step (k) is performed by removing said second sacrificial layer (29) to release said second substrate (6).
7. A process according to claim 6 wherein step (i) is performed by:
  - providing a third substrate (28) of a material which is to form said precursor layer (17);
  - forming said second sacrificial layer (29) on said third substrate (28);
  - attaching said third substrate (28) to said second substrate (6) by means of said second sacrificial layer (29); and
  - thinning said third substrate (28) to produce said precursor layer (17).
8. A process according to claim 7 wherein said third substrate (28) is of crystalline material.
9. A process according to claim 6 wherein said second substrate (1), second sacrificial layer (29) and precursor layer (17) are comprised by a silicon-on-insulator (SOI) substrate.
10. A process according to any preceding claim 4 to 9 wherein said step (l) of patterning said precursor layer (17) is performed after said step (k) of removing said second substrate (6).
11. A process according to any preceding claim 4 to 9, wherein said step (l) of patterning said precursor layer (17) is performed before said step (j) of attaching said precursor layer (17) to said first substrate (1).
12. A process according to any preceding claim 2 to 11 wherein said first sacrificial layer (7) is of resin and

the step of attaching to said first substrate (1) is performed by using the first sacrificial layer and an adhesive layer.

13. A process according to claim 12 wherein said resin comprises a photoresist.
14. A process according to claim 13 wherein said photoresist contains cyclised rubber.
15. A process according to any preceding claim 2 to 11 wherein said first sacrificial layer (7) is a composite of two layers (7c, 7d) one (7c) which acts as a spacer layer for defining the thickness of said air-space (4), and the other one (7d) of which is an adherent layer for the step of attaching.
16. A process according to claim 15 wherein the one layer (7d) is of a cured first photoresist, and the other one layer (7c) is a curable photoresist cured during the step of attaching.
17. A process according to any preceding claim 12 to 16 wherein said remainder part of said first sacrificial layer (7) is removed by dry etching.
18. A process according to claim 17 wherein said dry etching is performed using an oxygen plasma.
19. A process according to any preceding claim 2 to 11 wherein said first sacrificial layer (7) is of a multilayer Langmuir-Blodgett film which prior to the step of attaching is provided on one or both of said first and second substrates (1, 6).
20. A process according to claim 19 wherein an electrostatic field is applied across said first sacrificial layer (7) for performing the step of attaching.
21. A process according to any preceding claim 12 to 14, 16, 19 and 20 wherein said remainder part of said first sacrificial layer (7) is removed by means of a solvent.
22. A process according to either of claims 19 or 20 wherein said remainder part of said first sacrificial layer (7) is removed by heating.
23. A process according to any of claims 6 to 8, or any claim depending directly or indirectly therefrom, wherein said second sacrificial layer (29) is of metal.
24. A process according to claim 23 wherein said precursor layer (17) is attached to said second substrate (6) by anodic bonding of said second sacrificial layer (29) of metal.
25. A process according to any preceding claim 1 to 24

wherein step (a) includes forming a patterned electrode (16) on said surface (5) of said beam member (2).

- 5 26. A process according to any preceding claim 2 to 11, wherein said first sacrificial layer (7) is provided on one or both of said first and second substrates (1, 6) prior to said step of attaching.

#### Patentansprüche

1. Verfahren zur Herstellung eines Mikroaufbaus mit einem Substrat (1), einem Balkenelement (2) und einer Stützeinrichtung (3), wobei das Balkenelement (2) über der Oberfläche des Substrats (1) durch die Stützeinrichtung (3) aufgehängt und von der Oberfläche durch eine Luftlücke (4) getrennt ist, das Verfahren weist in der nachstehenden Reihenfolge die Schritte

(a) Bereitstellen eines ersten Substrats (1) mit einer ersten Opferschicht (7) über dessen Oberfläche und eines Balkenelementes (2) auf der ersten Opferschicht (7);

(b) Entfernen des Teiles der ersten Opferschicht (7), der nicht zwischen dem Balkenelement (2) und dem Substrat (1) liegt;

(c) Ausbilden einer Stützeinrichtung (3) auf dem Substrat (1) und über dem Balkenelement (2), wobei die innere Oberfläche der Stützeinrichtung (3) an die Oberfläche des ersten Substrats und an die der Oberfläche des ersten Substrats abgewandte Oberfläche (5) des Balkenelementes (2) angebracht ist; und

(d) Entfernen des verbleibenden Teils der ersten Opferschicht (7), wobei das Balkenelement (2) von der Oberfläche des ersten Substrats durch eine Luftlücke (4) getrennt zurückbleibt, ohne daß ein Teil des Balkenelementes (2) unmittelbar von unten gestützt wird,

auf.

2. Verfahren nach Anspruch 1, wobei der Schritt (a) beinhaltet, daß

(e) ein zweites Substrat (6) aus einem Werkstoff bereitgestellt wird, aus dem das Balkenelement (2) ausgebildet wird;

(f) eine Erhöhung (9) mit der Struktur des Balkenelementes in der Oberfläche des zweiten Substrats (6) erzeugt wird;

(g) die Erhöhung (9) mit der Struktur des Balkenelementes mittels der ersten Opferschicht (7) an dem ersten Substrat (1) angebracht wird; und

(h) das zweite Substrat (6) zur Ausbildung des

Balkenelementes (2) aus der Erhöhung (9) mit der Struktur des Balkenelementes dünner gemacht wird.

3. Verfahren nach Anspruch 2, wobei das zweite Substrat (6) aus einem kristallinen Werkstoff ist.
4. Verfahren nach Anspruch 1, wobei Schritt (a) beinhaltet, daß
  - (i) ein zweites Substrat (6) bereitgestellt wird, das eine Vorläuferschicht (17) aus einem Werkstoff darauf aufweist, aus dem das Balkenelement (2) ausgebildet wird;
  - (j) die Vorläuferschicht (17) an das erste Substrat (1) mittels der ersten Opferschicht (7) angebracht wird;
  - (k) das zweite Substrat (6) entfernt wird; und
  - (l) das Balkenelement (2) durch Strukturierung der Vorläuferschicht (17) hergestellt wird.
5. Verfahren nach Anspruch 4, wobei die auf dem zweiten Substrat (6) bereitgestellte Vorläuferschicht (17) durch thermische Oxidation des zweiten Substrats (6) hergestellt wird.
6. Verfahren nach Anspruch 4, wobei die auf dem zweiten Substrat (6) bereitgestellte Vorläuferschicht (17) an dieser mittels einer zweiten Opferschicht (29) angebracht wird, und Schritt (k) durchgeführt wird, indem die zweite Opferschicht (29) zum Freigeben des zweiten Substrats (6) entfernt wird.
7. Verfahren nach Anspruch 6, wobei Schritt (i) durchgeführt wird, indem
  - ein drittes Substrat (28) aus einem Werkstoff bereitgestellt wird, aus dem die Vorläuferschicht (17) ausgebildet wird;
  - die zweite Opferschicht (29) auf dem dritten Substrat (28) ausgebildet wird;
  - das dritte Substrat (28) an dem zweiten Substrat (6) mittels der zweiten Opferschicht (29) angebracht wird; und
  - das dritte Substrat (28) zur Herstellung der Vorläuferschicht (17) dünner gemacht wird.
8. Verfahren nach Anspruch 7, wobei das dritte-Substrat (28) aus einem kristallinen Werkstoff ist.
9. Verfahren nach Anspruch 6, wobei das zweite Substrat (1), die zweite Opferschicht (29) und die Vorläuferschicht (17) durch ein Silizium-auf-Isolator-Substrat (SOI) zusammengesetzt sind.
10. Verfahren nach einem beliebigen der vorstehenden Ansprüche 4 bis 9, wobei der Schritt (l) zur Struktur-

rierung der Vorläuferschicht (17) nach dem Schritt (k) zur Entfernung des zweiten Substrats (6) durchgeführt wird.

11. Verfahren nach einem beliebigen der vorstehenden Ansprüche 4 bis 9, wobei der Schritt (l) zur Strukturierung der Vorläuferschicht (17) vor Schritt (j) zum Anbringen der Vorläuferschicht (17) an dem ersten Substrat (1) durchgeführt wird.
12. Verfahren nach einem beliebigen der vorstehenden Ansprüche 2 bis 11, wobei die erste Opferschicht (7) aus Harz ist und der Schritt zum Anbringen an das erste Substrat (1) unter Verwendung der ersten Opferschicht und einer Haftschrift durchgeführt wird.
13. Verfahren nach Anspruch 12, wobei das Harz einen Fotolack aufweist.
14. Verfahren nach Anspruch 13, wobei der Fotolack cyclisierten Gummi enthält.
15. Verfahren nach einem beliebigen der vorstehenden Ansprüche 2 bis 11, wobei die erste Opferschicht (7) eine Zusammensetzung aus zwei Schichten (7c, 7d) ist, von denen die eine (7c) als Abstandsschicht zur Definition der Dicke der Luftlücke (4) und die andere (7d) als Haftschrift für den Schritt zum Anbringen wirkt.
16. Verfahren nach Anspruch 15, wobei die eine Schicht (7d) aus einem ersten ausgehärteten Fotolack ist, und die andere Schicht (7c) aus einem härtbaren Fotolack ist, der während dem Schritt zum Anbringen gehärtet wird.
17. Verfahren nach einem beliebigen der Ansprüche 12 bis 16, wobei der verbleibende Teil der ersten Opferschicht (7) durch Trockenätzen entfernt wird.
18. Verfahren nach Anspruch 17, wobei das Trockenätzen unter Verwendung eines Sauerstoffplasmas durchgeführt wird.
19. Verfahren nach einem beliebigen der vorstehenden Ansprüche 2 bis 11, wobei die erste Opferschicht (7) eine vielschichtige Langmuir-Blodgett-Schicht aufweist, die vor dem Schritt zum Anbringen auf einem oder beiden von dem ersten und zweiten Substrat (1, 6) bereitgestellt wird.
20. Verfahren nach Anspruch 19, wobei ein elektrostatisches Feld an die erste Opferschicht (7) zur Durchführung des Schrittes zum Anbringen angelegt wird.
21. Verfahren nach einem beliebigen der vorstehenden

Ansprüche 12 bis 14, 16, 19 und 20, wobei der verbleibende Teil der ersten Opferschicht (7) mittels einer Lösung entfernt wird.

22. Verfahren nach einem der Ansprüche 19 oder 20, wobei der verbleibende Teil der ersten Opferschicht (7) durch Erwärmung entfernt wird.
23. Verfahren nach einem beliebigen der Ansprüche 6 bis 8, oder einem beliebigen davon direkt oder indirekt abhängigen Anspruch, wobei die zweite Opferschicht (29) aus Metall ist.
24. Verfahren nach Anspruch 23, wobei die Vorläuferschicht (17) an das zweite Substrat (6) durch anodisches Verbinden der zweiten Opferschicht (29) aus Metall angebracht wird.
25. Verfahren nach einem beliebigen der vorstehenden Ansprüche 1 bis 24, wobei Schritt (a) die Ausbildung einer strukturierten Elektrode (16) auf der Oberfläche (5) des Balkenelementes (2) beinhaltet.
26. Verfahren nach einem beliebigen der vorstehenden Ansprüche 2 bis 11, wobei die erste Opferschicht (7) auf einem oder beiden von dem ersten und zweiten Substrat (1, 6) vor dem Schritt zum Anbringen bereitgestellt wird.

#### Revendications

1. Procédé de fabrication d'une microstructure comportant un substrat (1), un élément poutre (2) et un moyen de support (3), dans lequel l'élément poutre (2) est suspendu au-dessus de la surface du substrat (1) par ce moyen de support (3) et est séparé de cette surface par un espace d'air (4), lequel procédé comprend l'exécution dans l'ordre suivant des étapes consistant à :
  - (a) se munir d'un premier substrat (1) ayant une première couche sacrificielle (7) sur sa surface et un élément poutre (2) sur cette première couche sacrificielle (7) ;
  - (b) éliminer la partie de cette couche sacrificielle (7) qui n'est pas prise en sandwich entre ledit élément poutre (2) et ce substrat (1) ;
  - (c) former un moyen de support (3) sur le substrat (1) et sur l'élément poutre (2) avec la surface intérieure de ce moyen de support (3) fixée sur la surface de ce premier substrat et sur la surface (5) dudit élément poutre (2) qui est plus éloignée de la surface de ce premier substrat ; et
  - (d) éliminer la partie restante de ladite première couche sacrificielle (7) pour laisser l'élément poutre (2) séparé de la surface dudit premier

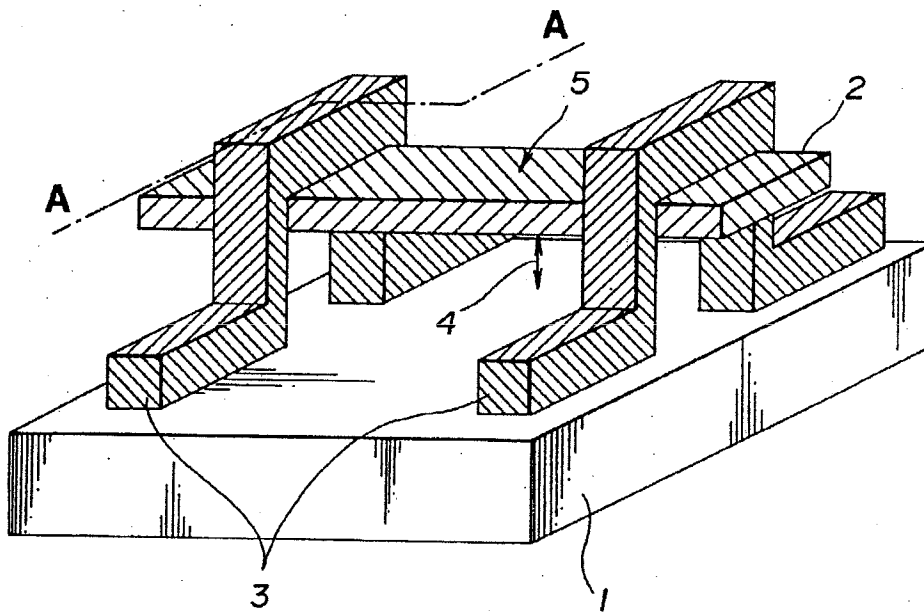
substrat par un espace d'air (4) sans qu'aucune partie de celui-ci ne soit directement supportée par en dessous.

2. Procédé selon la revendication 1, dans lequel l'étape (a) comprend :
  - (e) le fait de se procurer un second substrat (6) de matière qui doit former ledit élément poutre (2) ;
  - (f) le fait de fabriquer une protubérance portant un dessin d'élément poutre (9) sur la surface de ce second substrat (6) ;
  - (g) le fait de fixer cette protubérance portant un dessin d'élément poutre (9) sur le premier substrat (1) au moyen de cette première couche sacrificielle (7) ; et
  - (h) le fait d'amincir le second substrat (6) pour former ledit élément poutre (2) à partir de cette protubérance portant un dessin d'élément poutre (9).
3. Procédé selon la revendication 2, dans lequel ledit second substrat (6) est en une matière cristalline.
4. Procédé selon la revendication 1, dans lequel l'étape (a) comprend :
  - (i) le fait de se procurer un second substrat (6) portant une couche jouant le rôle de précurseur (17) qui est en une matière devant former ledit élément poutre (2) ;
  - (j) le fait de fixer cette couche jouant le rôle de précurseur (17) sur ce premier substrat (1) au moyen de ladite première couche sacrificielle (7) ;
  - (k) le fait d'éliminer ce second substrat (6) ; et
  - (l) le fait de fabriquer ledit élément poutre (2) par mise en forme de ladite couche jouant le rôle de précurseur (17).
5. Procédé selon la revendication 4, dans lequel la couche jouant le rôle de précurseur (17) apportée sur ce second substrat (6) est fabriquée par oxydation thermique de ce second substrat (6).
6. Procédé selon la revendication 4, dans lequel ladite couche jouant le rôle de précurseur (17) apportée sur le second substrat (6) est fixée sur celui-ci au moyen d'une seconde couche sacrificielle (29), et l'étape (k) est effectuée en éliminant cette seconde couche sacrificielle (29) pour libérer le second substrat (6).
7. Procédé selon la revendication 6, dans lequel l'étape (i) est effectuée :
  - en réalisant un troisième substrat (28) d'une

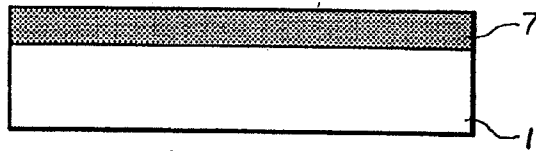


- matière qui doit former la couche jouant le rôle de précurseur (17);  
 en formant cette seconde couche sacrificielle (29) sur le troisième substrat (28);  
 en fixant ce troisième substrat (28) sur le second substrat (6) au moyen de ladite seconde couche sacrificielle (29); et  
 en amincissant ce troisième substrat (28) pour produire la couche servant de précurseur (17).
8. Procédé selon la revendication 7, dans lequel ledit troisième substrat (28) est en une matière cristalline.
9. Procédé selon la revendication 6, dans lequel le second substrat (6), la seconde couche sacrificielle (29) et la couche jouant le rôle de précurseur (17) sont constitués d'un substrat silicium-sur-isolant (SOI).
10. Procédé selon l'une quelconque des revendications 4 à 9 précédentes, dans lequel l'étape (1) de formation d'un dessin sur la couche jouant le rôle de précurseur (17) est effectuée après l'étape (k) d'élimination de ce second substrat (6).
11. Procédé selon l'une quelconque des revendications 4 à 9 précédentes, dans lequel l'étape (1) de formation d'un dessin sur la couche jouant le rôle de précurseur (17) est effectuée avant l'étape (j) de fixation de cette couche jouant le rôle de précurseur (17) sur le premier substrat (1).
12. Procédé selon l'une quelconque des revendications 2 à 11 précédentes, dans lequel la première couche sacrificielle (7) est constituée d'une résine et l'étape de fixation sur ce premier substrat (1) est effectuée en utilisant la première couche sacrificielle et une couche adhésive.
13. Procédé selon la revendication 12, dans lequel cette résine comprend un photorésist.
14. Procédé selon la revendication 13, dans lequel ce photorésist contient un caoutchouc cyclisé.
15. Procédé selon l'une quelconque des revendications 2 à 11 précédentes, dans lequel cette première couche sacrificielle (7) est un composite de deux couches (7c, 7d) dont l'une (7c) se comporte comme une couche d'espacement pour définir l'épaisseur de cet espace d'air (4), et l'autre (7d) est une couche adhérente pour l'étape de fixation.
16. Procédé selon la revendication 15, dans lequel ladite une couche (7d) est constituée d'un premier photorésist durci, et l'autre couche (7c) est un photorésist durcissable durci au cours de l'étape de fixation.
17. Procédé selon l'une quelconque des revendications 12 à 16 précédentes, dans lequel ladite partie restante de cette première couche sacrificielle (7) est éliminée par décapage à sec.
18. Procédé selon la revendication 17, dans lequel ce décapage à sec est effectué en utilisant un plasma d'oxygène.
19. Procédé selon l'une quelconque des revendications 2 à 11 précédentes, dans lequel la première couche sacrificielle (7) est un film de Langmuir-Blodgett multicouche qui est appliqué, avant l'étape de fixation, sur un de ces premier et second substrats (1, 6), ou sur les deux.
20. Procédé selon la revendication 19, dans lequel un champ électrostatique est appliqué à travers cette première couche sacrificielle (7) pour effectuer l'étape de fixation.
21. Procédé selon l'une quelconque des revendications 12 à 14, 16, 19 et 20 précédentes, dans lequel ladite partie restante de cette première couche sacrificielle (7) est éliminée au moyen d'un solvant.
22. Procédé selon l'une quelconque des revendications 19 ou 20, dans lequel ladite partie restante de ladite première couche sacrificielle (7) est éliminée par chauffage.
23. Procédé selon l'une quelconque des revendications 6 à 8, ou selon n'importe quelle revendication dépendant directement ou indirectement de celles-ci, dans lequel ladite seconde couche sacrificielle (29) est en métal.
24. Procédé selon la revendication 23, dans lequel la couche jouant le rôle de précurseur (17) est fixée sur le second substrat (6) par liaison anodique de ladite seconde couche sacrificielle (29) en métal.
25. Procédé selon l'une quelconque des revendications 1 à 24 précédentes, dans lequel l'étape (a) comprend la formation d'une électrode (16) portant des dessins sur ladite surface (5) dudit élément poutre (2).
26. Procédé selon l'une quelconque des revendications 2 à 11 précédentes, dans lequel la première couche sacrificielle (7) est prévue sur l'un des premier et second substrats précités (1, 6) ou sur les deux avant cette étape de fixation.

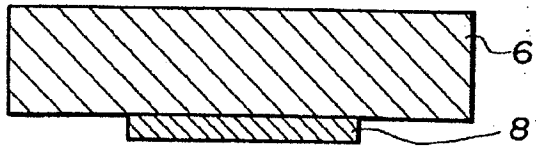
FIG.1



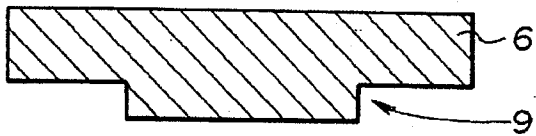
**FIG.2(A)**



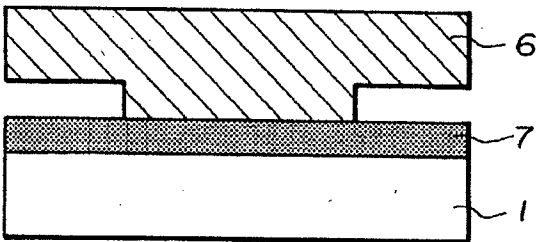
**FIG.2(B)**



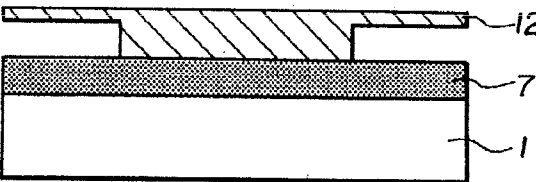
**FIG.2(C)**



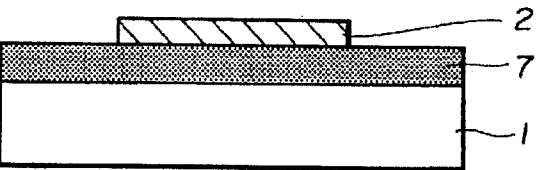
**FIG.2(D)**



**FIG.2(E)**



**FIG.2(F)**



**FIG.2(G)**

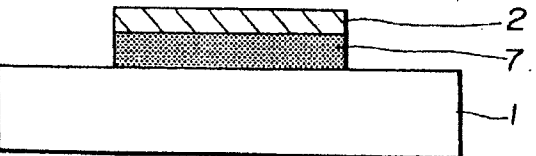


FIG.2(H)

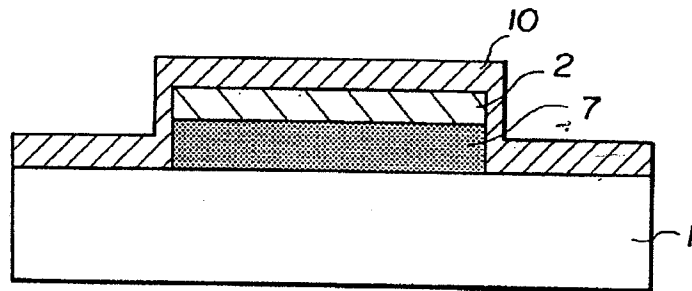


FIG.2(I)

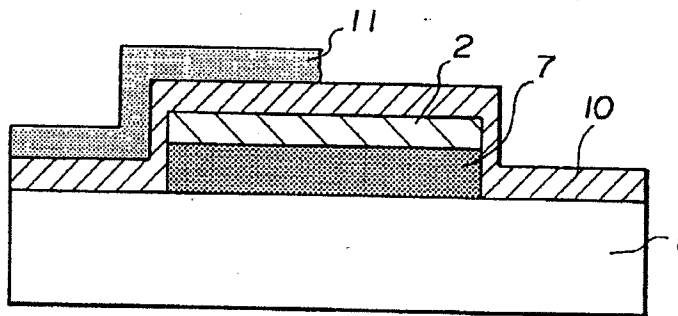


FIG.2(J)

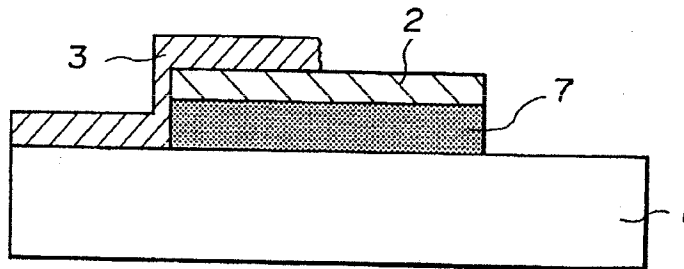
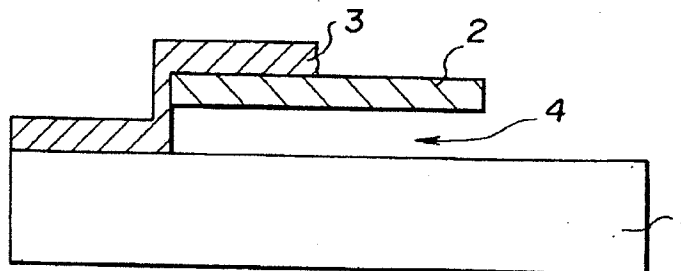


FIG.2(K)



**FIG.3**

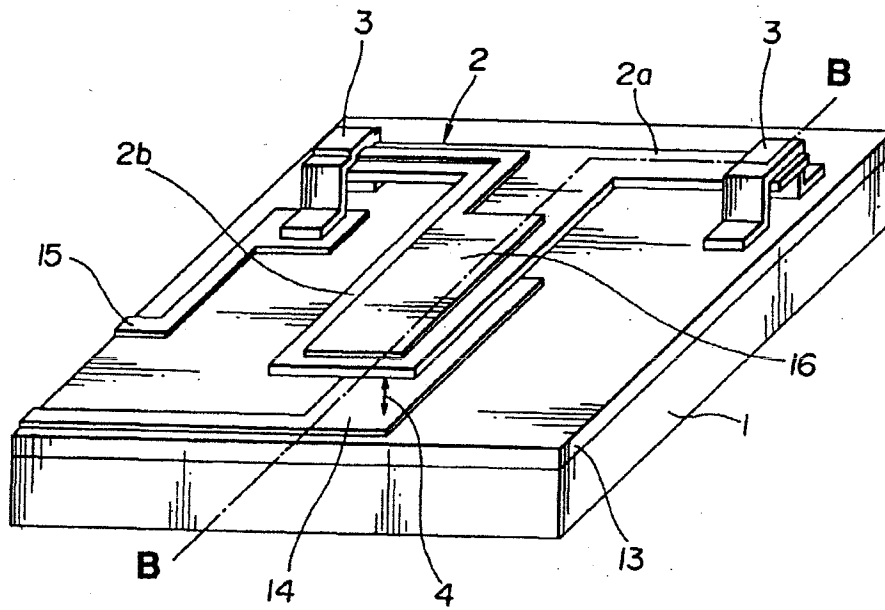


FIG.4(A)

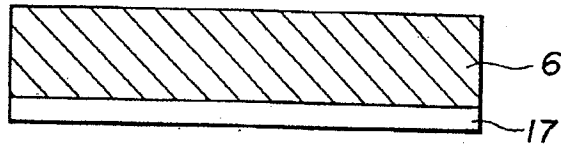


FIG.4(B)

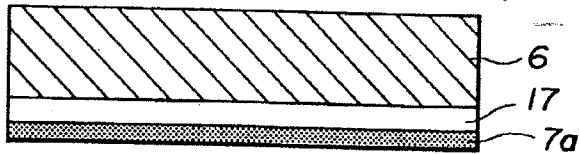


FIG.4(C)

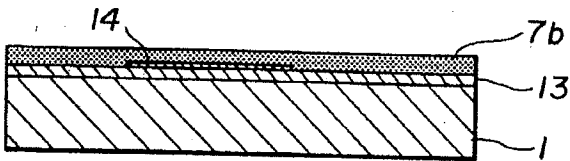


FIG.4(D)

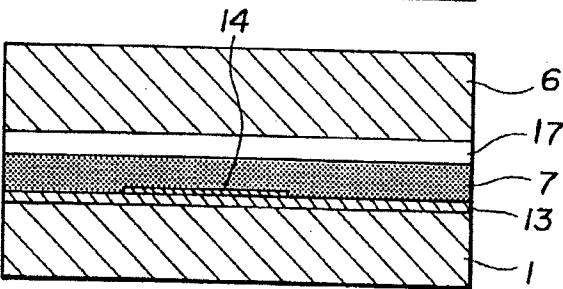


FIG.4(E)

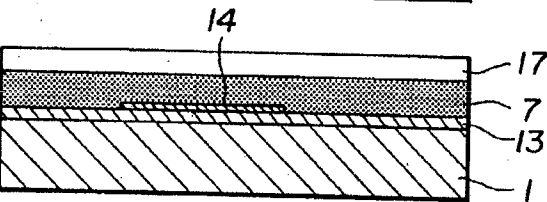


FIG.4(F)

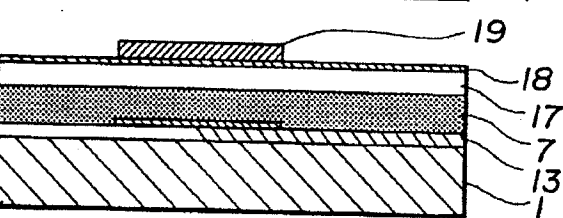


FIG.4(G)

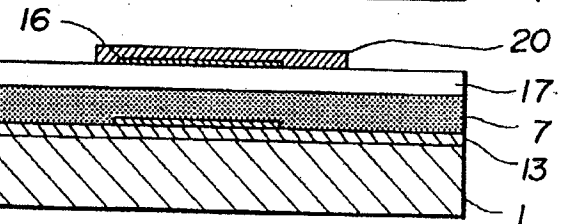


FIG.4(H)

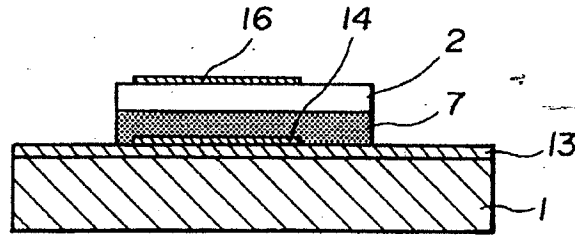


FIG.4(I)

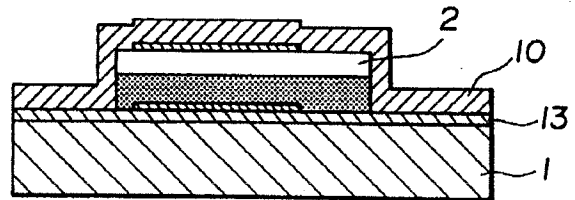


FIG.4(J)

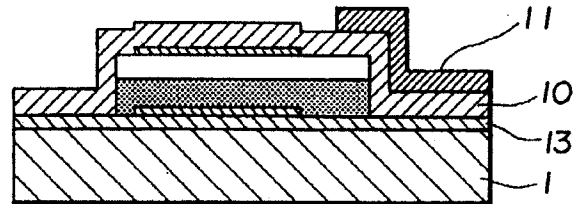


FIG.4(K)

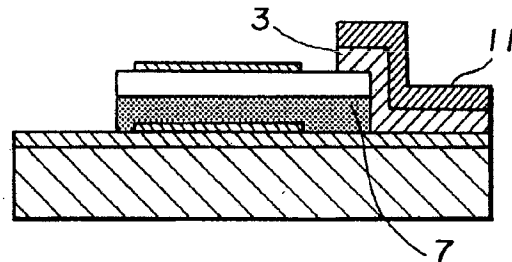


FIG.4(L)

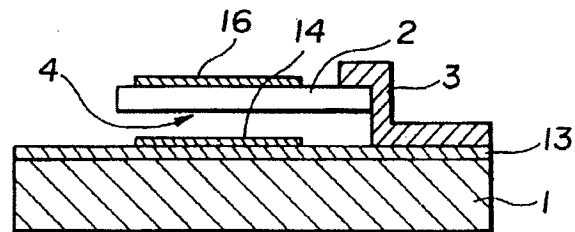
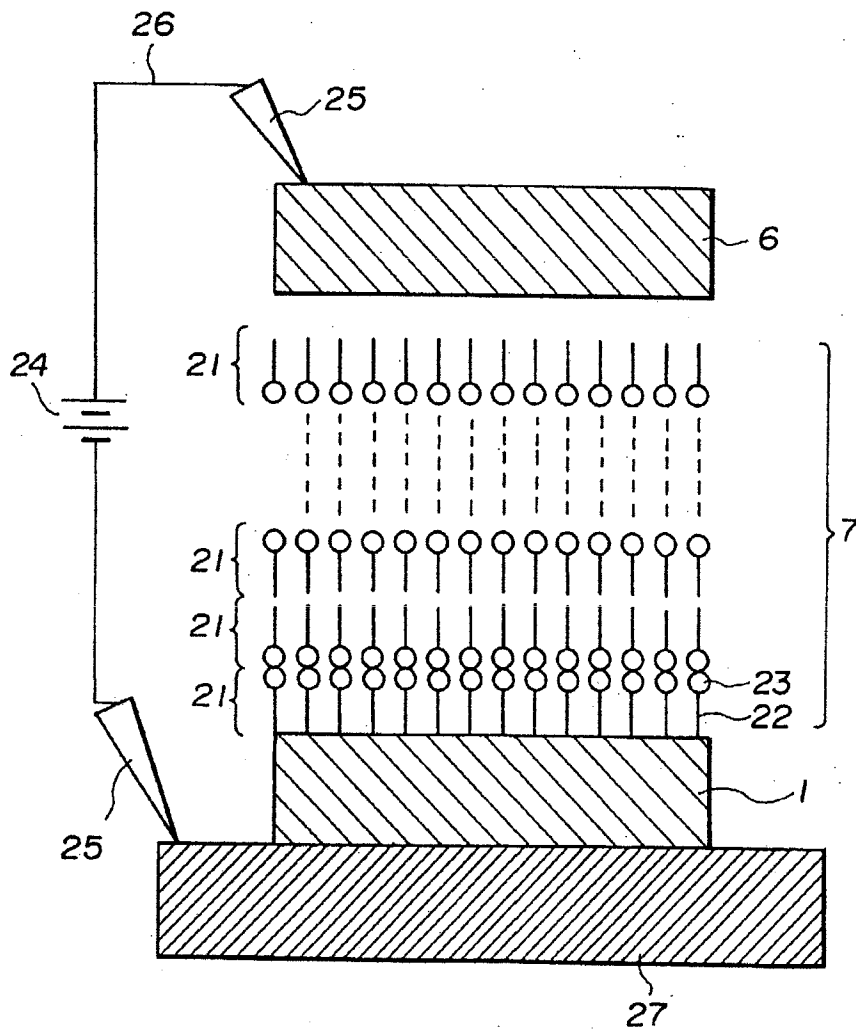
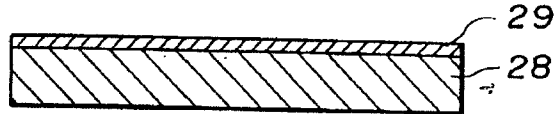


FIG.5

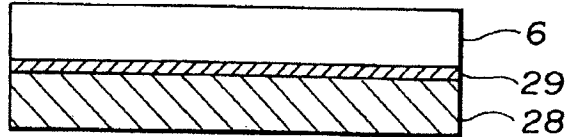




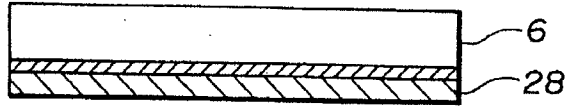
**FIG.6(A)**



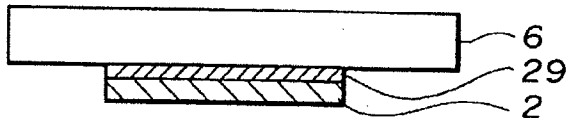
**FIG.6(B)**



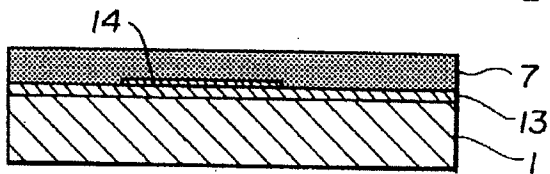
**FIG.6(C)**



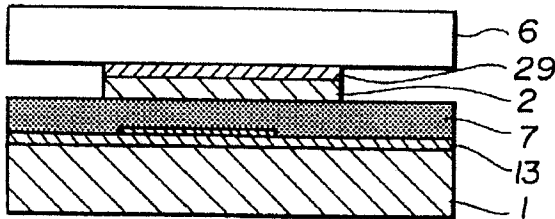
**FIG.6(D)**



**FIG.6(E)**



**FIG.6(F)**



**FIG.6(G)**

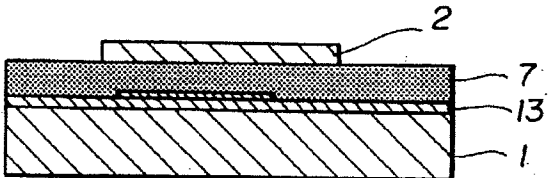


FIG.6(H)

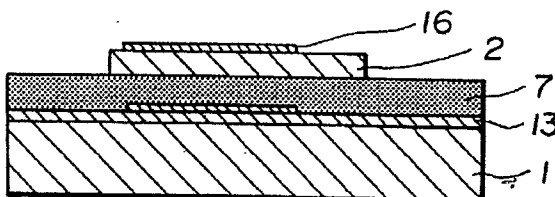


FIG.6(I)

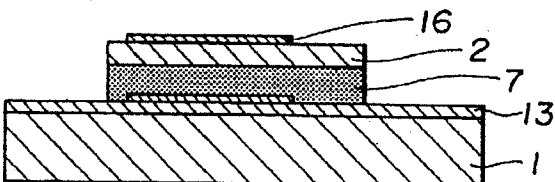


FIG.6(J)

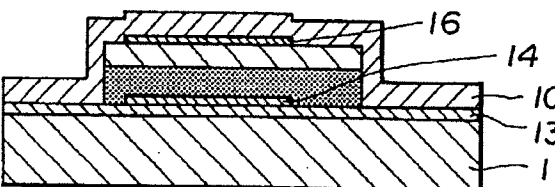


FIG.6(K)

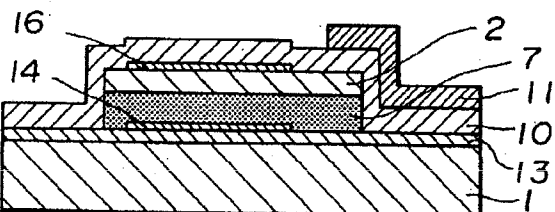


FIG.6(L)

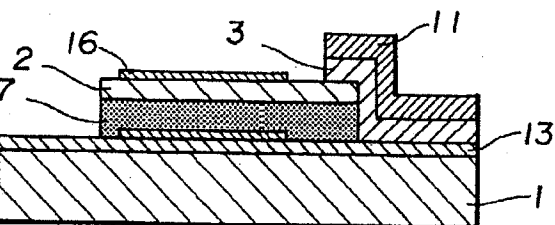


FIG.6(M)

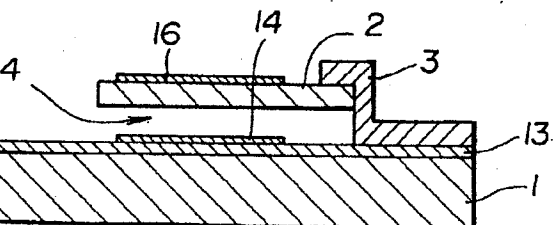
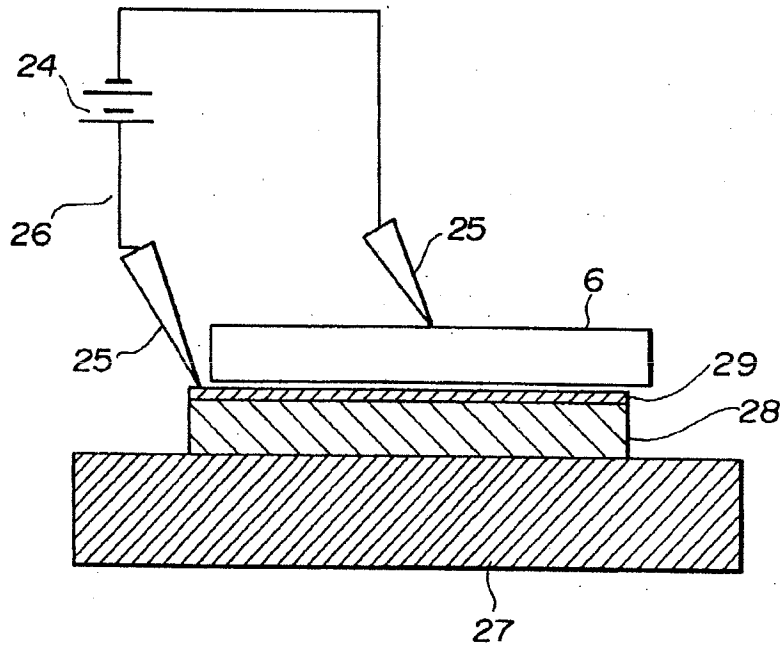
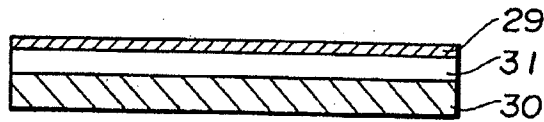


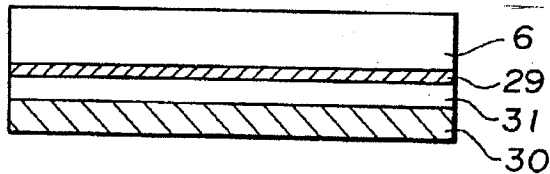
FIG.7



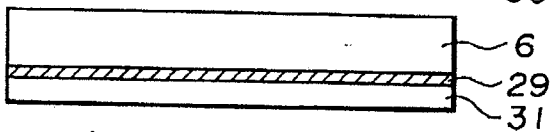
**FIG.8(A)**



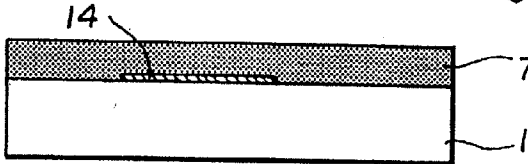
**FIG.8(B)**



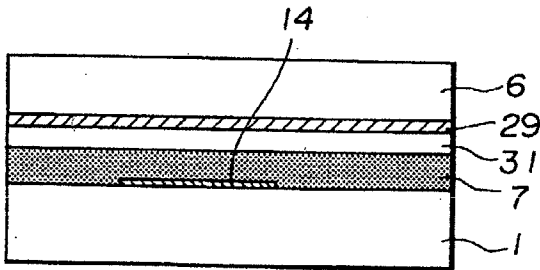
**FIG.8(C)**



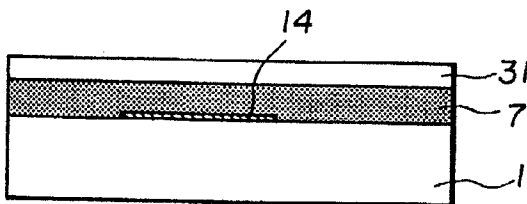
**FIG.8(D)**



**FIG.8(E)**



**FIG.8(F)**



**FIG.8(G)**

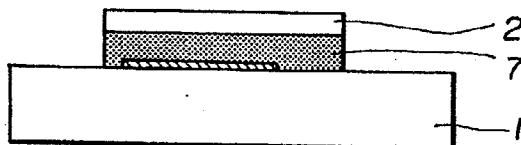


FIG.8(H)

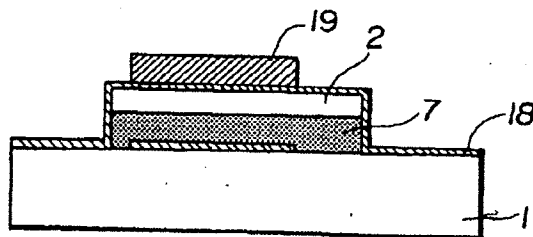


FIG.8(I)

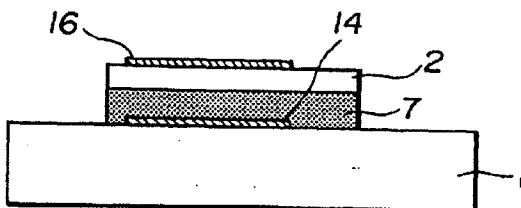


FIG.8(J)

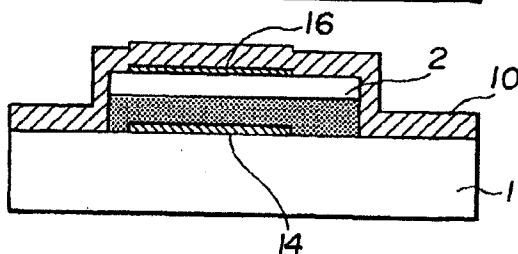


FIG.8(K)

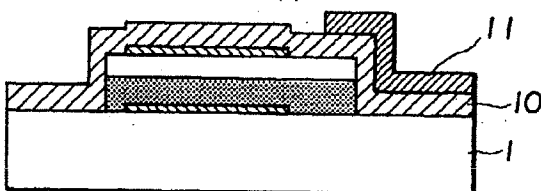


FIG.8(L)

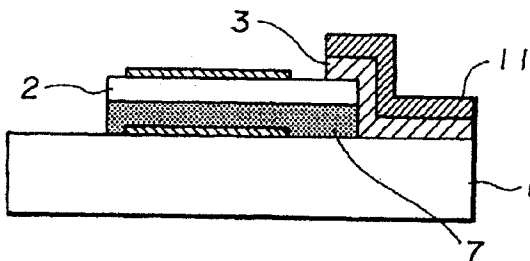
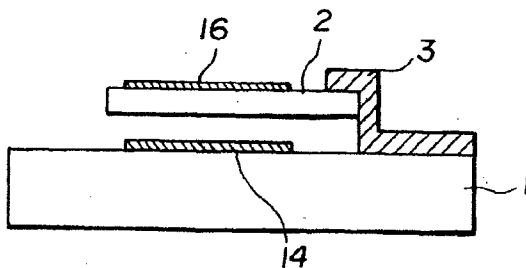
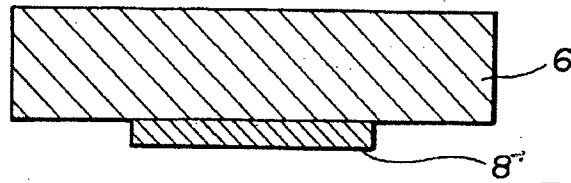


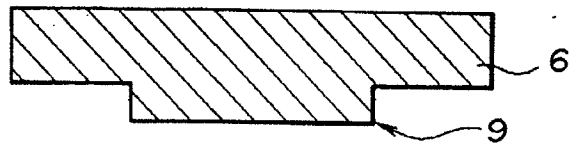
FIG.8(M)



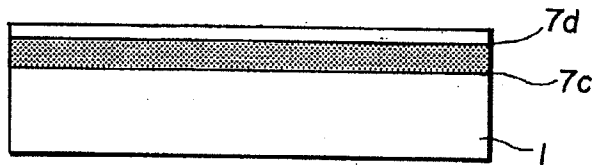
**FIG.9(A)**



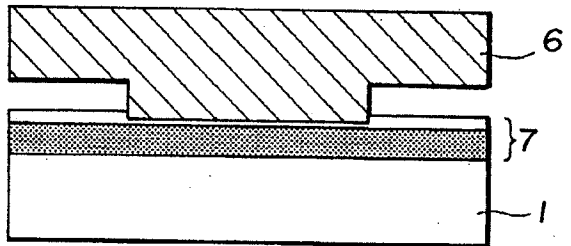
**FIG.9(B)**



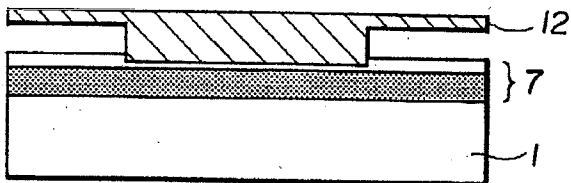
**FIG.9(C)**



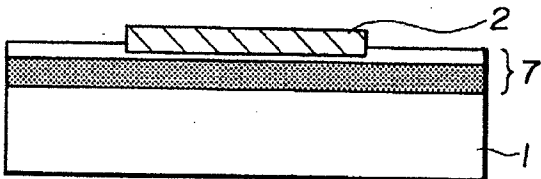
**FIG.9(D)**



**FIG.9(E)**



**FIG.9(F)**



**FIG.9(G)**

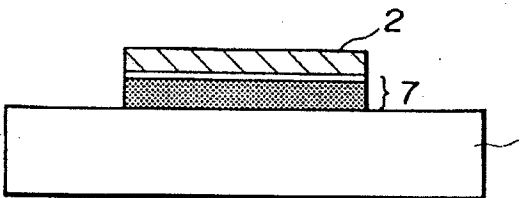


FIG.9(H)

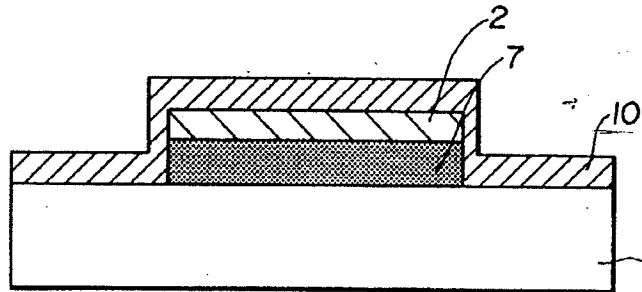


FIG.9(I)

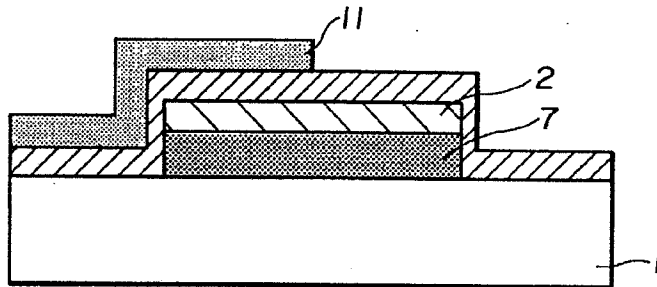


FIG.9(J)

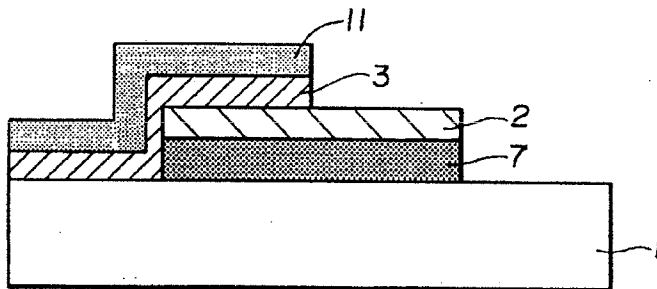
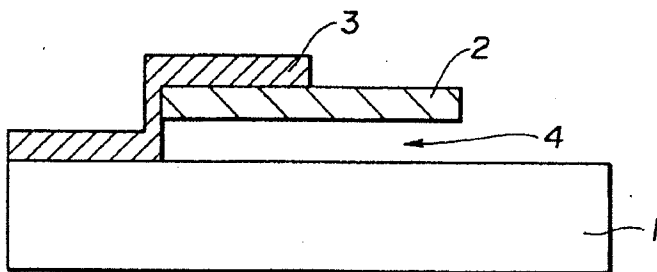


FIG.9(K)



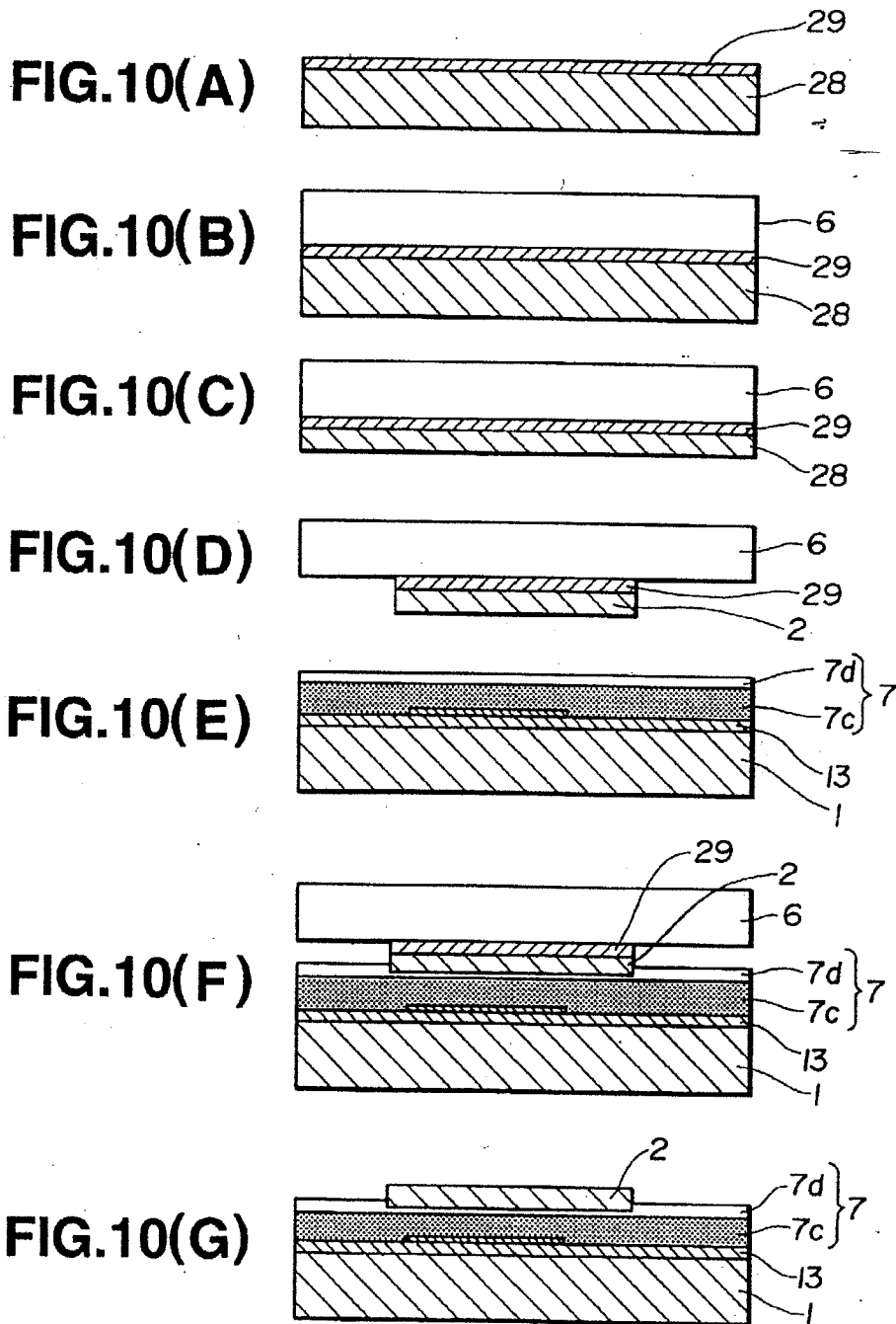




FIG.10(H)

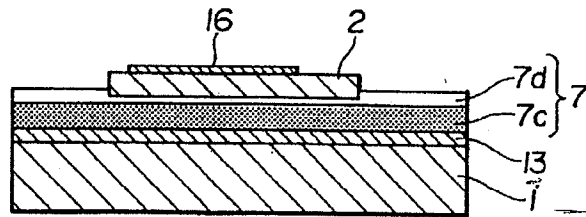


FIG.10(I)

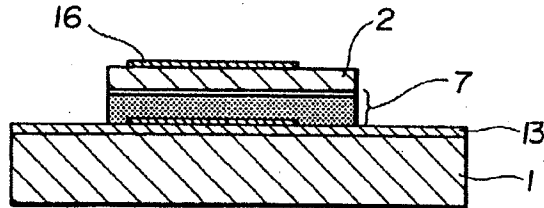


FIG.10(J)

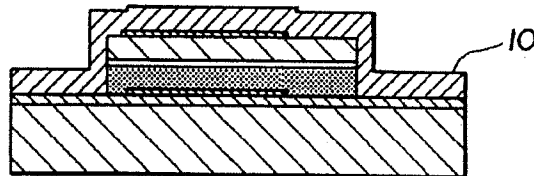


FIG.10(K)

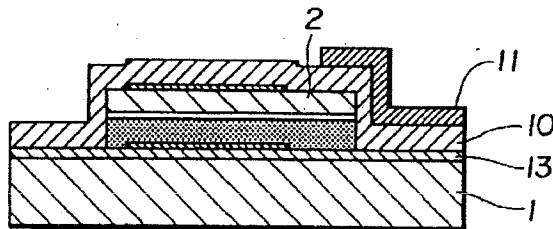


FIG.10(L)

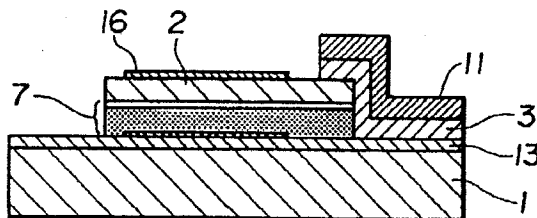


FIG.10(M)

